

# DV11

CABLE TST+MAN PARAM IN  
MD-11-DZDVE-B

EP-DZDVE-B-DL-A

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MADE IN U.S.A.

## IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZD/E-9-C  
PRODUCT NAME: MODEM CONTROL AND CABLE TESTS PLUS MANUAL REPAIR  
DATE RELEASED: 21-APRIL-1976  
MAINTAINER: DIAGNOSTICS  
AUTHOR: JOHN EGOLF

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## 1. ABSTRACT

THE FUNCTION OF THE DV11 DIAGNOSTICS ARE TO VERIFY THAT THE OPTION OPERATES ACCORDING TO SPECIFICATIONS. THE DIAGNOSTICS VERIFY THAT THERE ARE NO MALFUNCTIONS AND THE ALL OPERATIONS OF THE DV11 ARE CORRECT IN ITS ENVIRONMENT.

PARAMETERS MAY BE SET TO ALERT DIAGNOSTICS AS TO THE DV11 CONFIGURATION BY USING THE "TRIAL" PROGRAM (DZDVE SA:210). ALL QUESTIONS SHOULD BE ANSWERED AND THEN EACH DIAGNOSTIC WILL "OVERLAY" THESE PARAMETERS WHICH ARE STORED IN THE "STATUS TABLE" (SEE SECTION 8.4A). THE ALTERNATIVE TO "TRIAL" PROGRAM IS "AUTO SIZING" (SEE SECTION 8.5).

DZDVE IS USED TO VERIFY THE CABLES USED FOR MODEM HOOK UP. MODEM BITS ARE TESTED AND INTERRUPTS ARE ALSO CHECKED. ALL SIGNALS ARE TESTED AND THE TURN AROUND IS EITHER THROUGH THE SINGLE LINE TESTER(H325) OR 16 LINE TURN AROUND(H861). ALL SIGNALS THAT ARE LOOPED AROUND BY THE TEST CONNECTER ARE CHECKED. MODEM CONTROL SIGNALS AND DV11 TRANSMITTER AND RECEIVER DATA IS CHECKED. ANY COMBINATION OF LINES MAY BE SELECTED AND THESE INTURN WILL BE TESTED INDIVIDUALLY.

PART 2 -THE MANUAL PARAMETER INPUT(TRIAL)- IS USED TO GET THE PARAMETERS INTO THE STATUS TABLE FOR REFERENCE BY THE DIAGNOSTIC IF "AUTO SIZING" DOES NOT WORK OR IS NOT DESIRED. STARTING ADDRESS IS AT 210 AND THE EXECUTION OF THE PROGRAM IS SELF EXPLANATORY. (ANSWER THE QUESTIONS).

CURRENTLY THERE ARE SIX OFF LINE DIAGNOSTICS THAT ARE TO BE RUN IN SEQUENCE TO INSURE THAT IF AN ERROR SHOULD OCCUR IT WILL BE DETECTED AT AN EARLY STAGE AND INSURING THAT DIAGNOSIS OF ERROR WILL BE IMMEDIATE TO PROBLEM

NOTE: ADDITIONAL DIAGNOSTICS MAY BE ADDED IN THE FUTURE.

THE SIX DIAGNOSTICS ARE:

1. DZDVA (REV) BASIS R/W TEST AND ROM INSTRUCTION EXERCISER.
2. DZDVB (REV) STATIC LINE CARD TESTS.
3. DZDVC (REV) 'FREE RUNNING' ROM TESTS PART 1.
4. DZDVD (REV) 'FREE RUNNING' ROM TESTS PART 2.
5. DZDVE (REV) MODEM CONTROL AND CABLE TESTS PLUS MANUAL PARAMETER INPUT (TRIAL PROGRAM).
6. DZDVF (REV) ASYNCHRONOUS LINE CARD TESTS.

## 2. REQUIREMENTS

## 2.1 EQUIPMENT

ANY PDP11 FAMILY CPU (WITH MINIMUM 9K MEMORY)  
 MSR 33 (OR EQUIVALENT)  
 DV11-AA MUX CNTRL UNIT  
 AT LEAST ONE OF THE FOLLOWING  
 DV11-BA 8 LINE SYNC MODULES  
 DV11-BB 8 LINE ASYNC MODULES  
 DV11-BC 4 SYNC LINES, 4 ASYNC LINES

2.2 STORAGE

PROGRAM WILL USE ALL 9K OF MEMORY EXCEPT WHERE ABL AND BOOTSTRAP LOADER RESIDE. LOCATION 1500 THRU 1736 ARE ESPECIALLY TO BE NOTED AND TO BE UNTOUCHED BY OPERATOR AFTER DV11 TRIAL PROGRAM HAS BEEN EXECUTED; OR AFTER THE 'AUTO SIZING' HAS BEEN DONE.

3. LOADING PROCEEDURE

3.1 METHOD

ALL PROGRAMS ARE IN ABSOLUTE FORMAT AND ARE LOADED USING THE ABSOLUTE LOADER. NOTE: IF THE DIAGNOSTICS ARE ON A MEDIA SUCH AS DISK, MAGTAPE, DECTAPE, OR CASSETTE; FOLLOW INSTRUCTIONS FOR THE MONITOR WHICH HAS BEEN PROVIDED ON THAT SPECIFIC MEDIA.

ABSOLUTE LOADER STARTING ADDRESS \*500

MEMORY \* SIZE

4K	17
9K	17
12K	17
15K	17
20K	17
24K	17
28K	15

3.1.1 PLACE ADDRESS OF ABS LOADER INTO SWITCH REGISTER.  
(ALSO PLACE 'HALT' SW UP)

3.1.2 DEPRESS 'LOAD ADDRESS' KEY ON CONSOLE AND RELEASE.

3.1.3 DEPRESS 'START KEY' ON CONSOLE AND RELEASE (PROGRAM SHOULD NOW BE LOADING INTO CPU)

Vertical text on the left margin, possibly a page number or reference code.















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8.4A MORE ON THAT 'STATUS TABLE' (1500-1736)

'MAP OF DV11 STATUS'

1500	175000
1502	000300
1504	000226
1506	000062
1510	000226
1512	000062
1514	004000
1516	000000
1520	004000
1522	000000

THE ABOVE INFORMATION WILL BE REPEATED FOR EACH OF UP TO 8 DV11'S IN THE SYSTEM (THESE WILL FOLLOW UNDER THIS TABLE). EXPLANATION:

1500 175000 THIS IS THE SYSTEM CONTROL REGISTER FOR THE 1ST DV11 IN THE SYSTEM.

1502 000300 THIS IS VECTOR 'A' FOR THE FIRST DV11 IN THE SYSTEM.

1504 000226 THIS REPRESENTS 'SYNC A' AND THE SOFTWARE STATUS FOR THE 1ST LINE CARD IN THE 1ST DV11. THE BITS ARE AS FOLLOWS:

BIT 15 SET: LINE CARD \*NOT INSTALLED (AND WONT BE TESTED)

BIT 14 SET: RESERVED

BIT 13 SET: RESERVED

BIT 12 SET: ONE SYNC, =0: TWO SYNC.

BIT 11 SET: ASYNC LINE CARD, =0 SYNC LINE CARD

BIT 10 SET: RESERVED

BIT 09 SET: BITS PER CHAR. (USED WITH BIT8)

BIT 08 SET: BITS PER CHAR. (USED WITH BIT9)

BIT09	BIT08	BITS PER CHAR.
0	0	8
0	1	7
1	0	6
1	1	5

BIT 07-00 SYNC 'A' FOR SPECIFIED LINE CARD.

1506 000062 THIS REPRESENTS 'SYNC B' FOR THE 1ST LINE CARD.

1510 000226 THIS IS 'SYNC A' AND LINE STATUS FOR THE 2ND LINE CARD. (FOR BITS DEFINATION SEE EXPLANATION FOR LINE CARD 1).

1512 000062 THIS IS 'SYNC B' FOR THE SECOND LINE CARD.

1514 000226 THIS IS 'SYNC A' AND LINE STATUS FOR THE 3RD LINE CARD. (FOR BITS DEFINATION SEE EXPLANATION FOR LINE CARD 1).

1516 000062 THIS IS 'SYNC B' FOR LINE CARD NO. 3.

1520 000226 THIS IS 'SYNC A' AND LINE STATUS FOR THE 4TH LINE CARD. (FOR BITS DEFINATION SEE EXPLANATION FOR LINE CARD 1).

1522 000062 THIS IS SYNC B FOR THE 4TH LINE CARD.

THE ABOVE IS REPEATED FOR EACH DV11 IN THE SYSTEM. THE TABLE IS FILLED BY AUTO SIZING OR BY THE MANUAL PARAMETER INPUT PROGRAM AS DESCRIBED PREVIOUSLY. ALSO IF DESIRED BY USER, THE LOCATIONS MAY BE ALTERED BY HAND (TOGGLED IN) TO SUIT THE SPECIFIC CONFIGURATION.

L01

DZDVE MACY11 27(732) 17-SEP-76 14:10 PAGE 12  
DZDVEB.P11

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## 8.5 \*\*\* METHOD OF AUTO SIZING \*\*\*

## 8.5.1 FINDING THE CONTROL STATUS REGISTER.

THE PROGRAM WILL START AT ADDRESS 175000 AND START 'REFERENCEING' ADDRESS. IF A NON-EX MEMORY TRAP OCCURES; THE POINTER (HOLDING 175000) IS UPDATED BY 10 AND THE ABOVE IS REPEATED UNTILL ADDRESS 175200 IS REACHED. IF A 'SLAVE SYNC RESPONSE' WAS ISSUED BY THE DV11 (OR ANY OTHER DEVICE) (NO NXM TRAP); POINTER PLUS 12 (SEL12) IS TESTED TO CONTAIN 177777 (MUST BE EXACTLY 177777); IF A TRAP IS ENCOUNTERED OR IF SEL12 DOES NOT CONTAIN 177777 THE ABOVE UPDATING IS PERFORMED. IF SEL12 WAS EQUAL TO 177777 THE POINTER IS STORED AWAY AND THE ROUTINE CONTINUES AS ABOVE:

NOTE: IF THE PROGRAM DOES NOT FIND YOUR DV11; SOMETHING IS WRONG AND AUTO SIZING SHOULD NOT BE DONE.

## 8.5.2 FINDING THE VECTOR

THE VECTOR AREA (ADDRESS 300-776) IS FILLED WITH THE INSTRUCTION IOT AND '+2' (NEXT ADDRESS). BIT7 AND BIT6 (RX INTERRUPT AND RX INTERRUPT IE) ARE SET INTO DVSCR REGISTER; A DELAY IS MADE AND IF NO INTERRUPT OCCURES (BECAUSE OF A BAD DV11) THE PROGRAM ASSUMES VECTOR ADDRESS 300 AND THE PROBLEM SHOULD BE FIXED IN THE DIAGNOSTIC. ONCE THE PROBLEM IS FIXED; THE PROGRAM SHOULD BE RE-SETUP AGAIN TO GET CORRECT VECTOR. IF AN INTERRUPT OCCURED; THE ADDRESS TO WHICH THE DV11 INTERRUPTED TO IS PICKED UP AND REPORTED AS THE VECTOR. NOTE: IF THE VECTOR REPORTED IS NOT THE VECTOR SET UP BY YOU; THERE IS A PROBLEM AND AUTO SIZING SHOULD NOT BE DONE.

## 8.5.3 PARAMETER ASSUMPTIONS.

SINCE TOO MUCH HARDWARE WOULD NEED TO BE TURNED ON TO SIZE THE REST OF THE PARAMETERS; THE PROGRAM MUST ASSUME THE REMAINING VARIATIONS. THE RESULT IF NOT TO YOUR SPECIFIC CONFIGURATION MAY BE ALTERED BY HANG (TOGGLE IN) IS DESIRED. IN THIS WAY 95% OF THE PARAMETER SETUP WAS DONE BY THE PROGRAM AND 5% BY YOU.  
THEREFORE:

- 1) ALL LINE CARDS(4) ARE ASSUMED TO BE INSTALLED.  
SET BIT15 OF STATUS MAP OF ANY (APPROIATE) LINE CARDS MISSING
- 2) TWO SYNC.  
SET BIT12 IF YOU HAVE A 4 LINE GROUP SET FOR 1 SYNC.
- 3) EIGHT BITS PER CHAR.  
ADJUST BITS 9 AND BIT 8 IN STATUS MAP FOR YOUR CORRECT CONFIG.
- 4) SYNCHRONOUS LINE CARDS INSTALLED  
SET BIT11 OF STATUS MAP FOR ASYNC LINE CARD AND ZERO SYNC CHARS.
- 5) SYNC "A"=226 AND SYNC "B"=062

IN ALL ADJUSTMENTS PLEASE REFER TO SECTION 8.4A FOR GREATER DETAIL.

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;\*MAINDEC-11-DZDVE-A/<377>/MODEM CONTROL TESTS AND MANUAL PARAMETER INPUT  
;\*COPYRIGHT 1972, DIGITAL EQUIPMENT CORP., MAYNARD, MASS. 01754  
\*-----\*

: STARTING PROCEDURE  
: LOAD PROGRAM  
: LOAD ADDRESS 000200  
: PRESS START  
: PROGRAM WILL TYPE "MAINDEC-11-DZDVE-A/<377>/MODEM CONTROL TESTS AND MANUAL PARA  
: PROGRAM WILL TYPE "R" TO INDICATE THAT TESTING HAS STARTED  
: AT THE END OF A PASS, PROGRAM WILL TYPE PASS COMPLETE MESSAGE  
: AND THEN RESUME TESTING

: SWITCH REGISTER OPTIONS  
:-----\*

100000  
040000  
020000  
010000  
004000  
002000  
001000  
000400  
000200  
000100  
000040  
000020  
000010  
000004  
000002  
000001

SW15=100000  
SW14=40000  
SW13=20000  
SW12=10000  
SW11=4000  
SW10=2000  
SW09=1000  
SW08=400  
SW07=200  
SW06=100  
SW05=40  
SW04=20  
SW03=10  
SW02=4  
SW01=2  
SW00=1

:=1, HALT ON ERROR  
:=1, LOOP ON CURRENT TEST  
:=1, INHIBIT ERROR TYPEOUT  
:=1, DELETE TYPEOUT/BELL ON ERROR.  
:=1, INHIBIT ITERATIONS  
:=1, ESCAPE TO NEXT TEST ON ERROR  
:=1, LOOP WITH CURRENT DATA  
:=1, LOOP ON ERROR  
:=1, DO "AUTO SIZING" ON INITIAL START UP.

: LOCK ON TEST SELECT  
: RESTART PROGRAM AT SELECTED TEST  
: RESELECT DV11 DESIRED ACTIVE  
: NOTE: THIS MUST NOT EXCEED ORIGINAL COUNT



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:-----
:TRAPCATCHER FOR ILLEGAL INTERRUPTS
:THE STANDARD "TRAP CATCHER" IS PLACED
:BEWEEN ADDRESS 0 TO ADDRESS 776.
:IT LOOKS LIKE "PC+2 HALT".
:-----
:*****

.=0
:STANDARD INTERRUPT VECTORS
:-----

.=24
.PFAIL          :POWER FAIL HANDLER
340             :SERVICE AT LEVEL 7
.HLT            :ERROR HANDLER
340             :SERVICE AT LEVEL 7
.TRPSRV        :GENERAL HANDLER DISPATCH SERVICE
340             :SERVICE AT LEVEL 7

.=40
.BLKW 1        :SAVE FOR ACT-11 OR DDP2
.BLKW 1        :RETURN ADDRESS IF UNDER ACT-11 OR DDP2
.BLKW 1        :SAVE FOR ACT-11 OR DDP2
LOGICAL        :FOR USE WITH ACT-11 OR DDP2

.=174
LIGHT: 0
.=176
SSWR: 0

.=200
IMP .START     :GO TO START OF PROGRAM

.=1000
040515 047111 MTITLE: .ASCIZ (377) 12) MAINDEC-11-ODDVE-A (377) MODEM CONTROL TESTS AND MANUAL PARAME

.=1200
LIGHTS:
SWR: 177570
177570
:INDIRECT POINTERS TO TELETYPE VECTORS AND REGISTERS
:-----

TKCSR: 177560 :TELETYPE KEYBOARD CONTROL REGISTER
TKOBR: 177562 :TELETYPE KEYBOARD DATA BUFFER
TPCSR: 177564 :TELEPRINTER CONTROL REGISTER
TPOBR: 177566 :TELEPRINTER DATA BUFFER

:PROGRAM CONTROL PARAMETERS
:-----

RETURN: 0      :SCOPE ADDRESS FOR LOOP ON TEST
NEXT: 0        :ADDRESS OF NEXT TEST TO BE EXECUTED
LOCK: 0        :ADDRESS FOR LOCK ON CURRENT DATA

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001344  
001346  
001350

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000000

:PROGRAM CONTROL FLAGS

INIFLG: .BYTE 0  
ERRFLG: .BYTE 0  
LOKFLG: .BYTE 0  
QV.FLG: .BYTE 0

:PROGRAM INITIALIZATION FLAG  
:ERROR OCCURED FLAG  
:LOCK ON CURRENT TEST FLAG  
:QUICK VERIFY FLAG.  
:ON FIRST PASS OF EACH DV11 ITERATIONS WILL BE SUPPRESSE

.EVEN  
SY=0

:DEFINITIONS FOR TRAP SUBROUTINE CALLS  
:POINTERS TO SUBROUTINES CAN BE FOUND  
:IN THE TABLE IMMEDIATLY FOLLOWING THE DEFINITIONS

::\*\*\*\*\*

TRPTAB:  
SCOPE=TRAP+0 :CALL TO SCOPE LOOP AND ITERATION HANDLER  
SCOPE :SCOPE  
SCOPI=TRAP+1 :CALL TO LOOP ON CURRENT DATA HANDLER  
SCOPI :SCOPI  
TYPE=TRAP+2 :CALL TO TELETYPE OUTPUT ROUTINE  
TYPE :TYPE  
INSTR=TRAP+3 :CALL TO ASCII STRING INPUT ROUTINE  
INSTR :INSTR  
INSTER=TRAP+4 :CALL TO INPUT ERROR HANDLER  
INSTER :INSTER  
PARAM=TRAP+5 :CALL TO NUMERICAL DATA INPUT ROUTINE  
PARAM :PARAM  
SAVOS=TRAP+6 :CALL TO REGISTER SAVE ROUTINE  
SAVOS :SAVOS  
RESOS=TRAP+7 :CALL TO REGISTER RESTORE ROUTINE  
RESOS :RESOS  
CONVRT=TRAP+10 :CALL TO DATA OUTPUT ROUTINE  
CONVRT :CONVRT  
CNVRT=TRAP+11 :CALL TO DATA OUTPUT ROUTINE WITHOUT CR-LF.  
CNVRT :CNVRT  
MSTCLR=TRAP+12 :CALL TO ISSUE A MASTER CLEAR  
MSTCLR :MSTCLR  
RAMCLR=TRAP+13 :CALL TO CLEAR THE RAMS  
RAMCLR :RAMCLR  
DELAY=TRAP+14 :CALL TO VARIABLE DELAY COUNTER  
DELAY :DELAY  
ROMCLK=TRAP+15 :CALL TO CLOCK ROM ONCE  
ROMCLK :ROMCLK  
DATACLK=TRAP+16 :CALL TO CLK DATA  
DATACLK :DATACLK

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:DV11 VECTOR AND REGISTER INDIRECT POINTERS

001352	000000	DVRVEC:	0	: POINTER TO DV11 RECEIVER INTERRUPT VECTOR
001354	000000	DVRLVL:	00	: POINTER TO DV11 RECEIVER INTERRUPT SERVICE PS
001356	000000	DVTVEC:	00	: POINTER TO DV11 TRANSMITTER INTERRUPT VECTOR
001360	000000	DVTLVL:	00	: POINTER TO DV11 TRANSMITTER INTERRUPT SERVICE PS
001362	000000	DVSCR:	00	: POINTER TO DV11 SYSTEM CONTROL REGISTER
001364	000000	DVSCRH:	00	: POINTER TO DV11 SYSTEM CONTROL REGISTER HIGH BYTE.
001366	000000	DVRIC:	00	: POINTER TO DV11 NEXT RECEIVED CHARACTER REGISTER
001370	000000	DVLCR:	00	: POINTER TO DV11 LINE PRAMETER REGISTER
001372	000000	DVSR:	00	: POINTER TO DV11 SECONDARY REGISTER SELECT REGISTER
001374	000000	DVSRSH:	00	: POINTER TO DV11 SECONDARY REGISTER SELECT HIGH BYTE.
001376	000000	DVSR:	00	: POINTER TO DV11 SECONDARY REGISTER ACCESS REGISTER
001400	000000	DVSR:	00	: POINTER TO DV11 SPECIAL FUNCTIONS REGISTER
001402	000000	DVNSR:	00	: POINTER TO DV11 NPR STATUS REGISTER
001404	000000	RESV16:	0	: POINTER TO RESERVED REGISTER.

:DV11 CONTROL INDICATORS FOR CURRENT DV11 UNDER TEST

001406	000	MASK.A:	.BYTE 000	: LAST CHAR TO TEST AND PARITY MASK FOR LINES 00-03
001407	000	MASK.B:	.BYTE 000	: LAST CHAR TO TEST AND PARITY MASK FOR LINES 04-07
001410	000	MASK.C:	.BYTE 000	: LAST CHAR TO TEST AND PARITY MASK FOR LINES 08-11
001411	000	MASK.D:	.BYTE 000	: LAST CHAR TO TEST AND PARITY MASK FOR LINES 12-15
001412	010	CLK.A:	.BYTE 9.	: NUMBER OF CLOCKS NEEDED FOR ONE CHAR FOR LINES 00-03
001413	010	CLK.B:	.BYTE 9.	: NUMBER OF CLOCKS NEEDED FOR ONE CHAR FOR LINES 04-07
001414	010	CLK.C:	.BYTE 9.	: NUMBER OF CLOCKS NEEDED FOR ONE CHAR FOR LINES 08-11
001415	010	CLK.D:	.BYTE 9.	: NUMBER OF CLOCKS NEEDED FOR ONE CHAR FOR LINES 12-15
001416	000000	L00.03:	000000	: PARAMETERS FOR LINES 00-03
001420	000000	L04.07:	000000	: PARAMETERS FOR LINES 04-07
001422	000000	L08.11:	000000	: PARAMETERS FOR LINES 08-11
001424	000000	L12.15:	000000	: PARAMETERS FOR LINES 12-15
001426	000000	SYNC2A:	000000	: SYNC 2
001430	000000	SYNC2B:	000000	:
001432	000000	SYNC2C:	000000	:
001434	000000	SYNC2D:	000000	:

:SUMMARY

:	MASK.X	040	5 BITS PER CHAR.
:		100	6 BITS PER CHAR.
:		200	7 BITS PER CHAR.
:		000	8 BITS PER CHAR.
:	CLK.X	005	5 BITS PER CHAR.
:		006	6 BITS PER CHAR.
:		007	7 BITS PER CHAR.
:		010	8 BITS PER CHAR.

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004                                     :DV11 STATUS TABLE AND ADDRESS ASSIGNMENTS
005                                     :-----
006
007                                     =1500
008 001500 000001 DV.MAP:
009 001500 000001 DVCRO0: .BLKW 1 :CONTROL STATUS REGISTER FOR DV11 NUMBER C0
010 001502 000001 DVTR00: .BLKW 1 :VECTOR "A" FOR DV11 NUMBER C0
011 001504 000001 DV00.A: .BLKW 1 :PARAMETER FOR LINES 00-03 FOR DV11 NUMBER C0
012 001506 000001 SYNA00: .BLKW 1 :SYNC TWO
013 001510 000001 DV00.B: .BLKW 1 :PARAMETER FOR LINES 04-07 FOR DV11 NUMBER C0
014 001512 000001 SYNBO0: .BLKW 1 :SYNC TWO
015 001514 000001 DV00.C: .BLKW 1 :PARAMETER FOR LINES 08-11 FOR DV11 NUMBER C0
016 001516 000001 SYNCO0: .BLKW 1 :SYNC TWO
017 001520 000001 DV00.D: .BLKW 1 :PARAMETER FOR LINES 12-15 FOR DV11 NUMBER C0
018 001522 000001 SYND00: .BLKW 1 :SYNC TWO
019
020 001524 000001 DVCRO1: .BLKW 1 :CONTROL STATUS REGISTER FOR DV11 NUMBER C1
021 001526 000001 DVTR01: .BLKW 1 :VECTOR "A" FOR DV11 NUMBER C1
022 001530 000001 DV01.A: .BLKW 1 :PARAMETER FOR LINES 00-03 FOR DV11 NUMBER C1
023 001532 000001 SYNA01: .BLKW 1 :SYNC TWO
024 001534 000001 DV01.B: .BLKW 1 :PARAMETER FOR LINES 04-07 FOR DV11 NUMBER C1
025 001536 000001 SYNBO1: .BLKW 1 :SYNC TWO
026 001540 000001 DV01.C: .BLKW 1 :PARAMETER FOR LINES 08-11 FOR DV11 NUMBER C1
027 001542 000001 SYNCO1: .BLKW 1 :SYNC TWO
028 001544 000001 DV01.D: .BLKW 1 :PARAMETER FOR LINES 12-15 FOR DV11 NUMBER C1
029 001546 000001 SYND01: .BLKW 1 :SYNC TWO
030
031 001550 000001 DVCRO2: .BLKW 1 :CONTROL STATUS REGISTER FOR DV11 NUMBER C2
032 001552 000001 DVTR02: .BLKW 1 :VECTOR "A" FOR DV11 NUMBER C2
033 001554 000001 DV02.A: .BLKW 1 :PARAMETER FOR LINES 00-03 FOR DV11 NUMBER C2
034 001556 000001 SYNA02: .BLKW 1 :SYNC TWO
035 001560 000001 DV02.B: .BLKW 1 :PARAMETER FOR LINES 04-07 FOR DV11 NUMBER C2
036 001562 000001 SYNBO2: .BLKW 1 :SYNC TWO
037 001564 000001 DV02.C: .BLKW 1 :PARAMETER FOR LINES 08-11 FOR DV11 NUMBER C2
038 001566 000001 SYNCO2: .BLKW 1 :SYNC TWO
039 001570 000001 DV02.D: .BLKW 1 :PARAMETER FOR LINES 12-15 FOR DV11 NUMBER C2
040 001572 000001 SYND02: .BLKW 1 :SYNC TWO
041
042 001574 000001 DVCRO3: .BLKW 1 :CONTROL STATUS REGISTER FOR DV11 NUMBER C3
043 001576 000001 DVTR03: .BLKW 1 :VECTOR "A" FOR DV11 NUMBER C3
044 001600 000001 DV03.A: .BLKW 1 :PARAMETER FOR LINES 00-03 FOR DV11 NUMBER C3
045 001602 000001 SYNA03: .BLKW 1 :SYNC TWO
046 001604 000001 DV03.B: .BLKW 1 :PARAMETER FOR LINES 04-07 FOR DV11 NUMBER C3
047 001606 000001 SYNBO3: .BLKW 1 :SYNC TWO
048 001610 000001 DV03.C: .BLKW 1 :PARAMETER FOR LINES 08-11 FOR DV11 NUMBER C3
049 001612 000001 SYNCO3: .BLKW 1 :SYNC TWO
050 001614 000001 DV03.D: .BLKW 1 :PARAMETER FOR LINES 12-15 FOR DV11 NUMBER C3
051 001616 000001 SYND03: .BLKW 1 :SYNC TWO
052
053 001620 000001 DVCRO4: .BLKW 1 :CONTROL STATUS REGISTER FOR DV11 NUMBER C4
054 001622 000001 DVTR04: .BLKW 1 :VECTOR "A" FOR DV11 NUMBER C4
055 001624 000001 DV04.A: .BLKW 1 :PARAMETER FOR LINES 00-03 FOR DV11 NUMBER C4
056 001626 000001 SYNA04: .BLKW 1 :SYNC TWO
057 001630 000001 DV04.B: .BLKW 1 :PARAMETER FOR LINES 04-07 FOR DV11 NUMBER C4
058 001632 000001 SYNBO4: .BLKW 1 :SYNC TWO
059 001634 000001 DV04.C: .BLKW 1 :PARAMETER FOR LINES 08-11 FOR DV11 NUMBER C4

```







```

991                                     :END OF PASS
992                                     :TYPE NAME OF TEST
993                                     :UPDATE PASS COUNT
994                                     :CHECK FOR EXIT TO ACT-11
995                                     :RESTART TEST
996
997 002436 000005 .EOP: RESET                                     :MAKE THE WORLD CLEAN AGAIN.
998 002440 005037 001234 CLR LSTERR                               :CLEAR LAST ERROR PC
999 002444 105037 001311 CLRB ERRFLG                            :CLEAR ERROR FLAG
1000 002450 005237 001230 INC PASCNT                             :UPDATE PASS COUNT
1001 002454 013777 001230 176516 MOV PASCNT, @LIGHTS        :DISPLAY PASS COUNT
1002 002452 104402 005145 TYPE ,MEPASS           :TYPE END PASS
1003 002466 104402 005330 TYPE ,MCSRX             :TYPE CSR
1004 002472 104411 002604 CNVRT ,XCSR              :SHOW IT
1005 002476 104402 005336 TYPE ,MVECX             :TYPE VECTOR
1006 002502 104411 002612 CNVRT ,XVEC              :SHOW IT
1007 002506 104402 005344 TYPE ,MPASSX            :TYPE PASSES
1008 002512 104411 002620 CNVRT ,XPASS             :SHOW IT
1009 002516 104402 005355 TYPE ,MERRX            :TYPE ERRORS
1010 002522 104411 002626 CNVRT ,XERR              :SHOW IT
1011 002526 105337 001303 DECB SAVNUM              :ARE ALL DEVICES TESTED?
1012 002532 001017 BNE RESTRT                            :BR IF NO.
1013 002534 112737 000377 001313 MOV B #377, QV.FLG        :SET THE QUICK VERIFY FLAG.
1014 002542 113737 001301 001303 MOV B DVNUM, SAVNUM      :RESTORE THE COUNT
1015 002550 013701 000042 MOV @#42, R1            :CHECK FOR ACT-11 OR DDP
1016 002554 001406 BEQ RESTRT                            :IF NOT, CONTINUE TESTING
1017 002556 000005 RESET                                     :STOP THE SHOW--CLEAR THE WORLD
1018
1019 002560 LOGICAL: JSR PC, (R1)
1020 002562 000240 NOP
1021 002564 000240 NOP
1022 002566 000240 NOP
1023 002570 000240 NOP
1024 002572 012737 005666 001214 RESTRT: MOV #CYCLE, RETURN
1025 002600 000137 005666 JMP CYCLE
1026 002604 000001 XCSR: 1
1027 002606 006 002 .BYTE 6,2
1028 002610 001362 DVSCR
1029 002612 000001 XVEC: 1
1030 002614 003 002 .BYTE 3,2
1031 002616 001352 DVRVEC
1032 002620 000001 XPASS: 1
1033 002622 006 002 .BYTE 6,2
1034 002624 001230 PASCNT
1035 002626 000001 XERR: 1
1036 002630 006 002 .BYTE 6,2
1037 002632 001232 ERRCNT
1038
1039                                     :SCOPE LOOP AND INTERATION HANDLER
1040                                     :-----
1041
1042 002634 .SCOPE:
1043 002634 022737 177570 001202 CMP #177570, SWR        :IS THERE A REAL SWR?
1044 002642 001411 BEQ 64$                               :BR IF YES
1045 002644 017746 176336 MOV @TKOBR, -(SP)      :SAVE KEYBOARD CHAR
1046 002650 042716 000200 BIC #BIT7, (SP)      :CLEAR PARITY BIT

```



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 DZDVEB.P11 GENERAL UTILITIES (TYPE OUT,ERROR,SCOPE,ETC.)

```

:047 002654 122726 000007      CMPB   #7,(SP)+      ;WAS IT CNTRL 'G' ?
1048 002660 001002          BNE    .+6         ;BR IF NO.
1049 002662 004737 004640      JSR    PC,SERV.G   ;SERVICE "CNTRL 'G'".
1050 002666 005037 001234      64$:  CLR    LSTERR   ;CLEAR LAST ERROR PC.
1051 002672 010016          MOV    RD,(SP)     ;SAVE RD ON THE STACK
1052 002674 032777 040000 176300 BIT    #BIT14,@SWR ;"LOOP ON THIS TEST"?
1053 002702 001407          BEQ    1$         ;BR IF NO. (IF LOCK SW01=1; THIS LOC =240)
1054 002704 000437          BR     3$         ;GOTO 3$ (IF LOCK SW01=1; THIS LOC =240)
1055 002706 105777 176272      TSTB  @TKCSR      ;KEYBOARD DONE?
1056 002712 100034          BPL    3$         ;BR IF NO. (LOCK: HIT KEY TO GOTO NEXT TEST)
1057 002714 017700 176266      MOV    @TKDBR,RD  ;CLEAR DONE BIT
1058 002720 000415          BR     2$         ;CONTINUE
1059 002722 032777 004000 176252 1$:  BIT    #SW11,@SWR ;DELETE ITERATION? (QUICK PASS)
1060 002730 001011          BNE    2$         ;BR IF YES
1061 002732 105737 001313      TSTB  QV.FLG      ;HAVE PASSES BEECOMPLETED?
1062 002736 001406          BEQ    2$         ;BR IF QUICK PASS.
1063 002740 005237 001224      INC    LPCNT      ;UPDATE ITERATION COUNTER
1064 002744 023737 001224 001222 CMP    LPCNT,ICOUNT ;ARE ALL ITERATIONS DONE??
1065 002752 001014          BNE    3$         ;BR IF NOT YET
1066 002754 105037 001311      2$:  CLRB  ERRFLG     ;PREPARE FOR NEW TEST
1067 002760 005037 001224      CLR    LPCNT      ;START ICOUNTER AT 0
1068 002764 005037 001220      CLR    LOCK
1069 002770 012737 000005 001222 MOV    #5,ICOUNT  ;RESET ITERATIONS
1070 002776 013737 001215 001214 MOV    NEXT,RETURN ;GET NEXT TEST
1071 003004 011600          3$:  MOV    (SP),RD   ;POP RD OFF OF THE STACK
1072 003006 022626          POP2SP ;FAKE AN "RTI"
1073 003010 000177 176200      JMP    @RETURN    ;GO DO THE TEST
1074 003014 001407          BRW:  1407
1075 003016 000437          BRX:  437
1076
1077          ;CHECK FOR FREEZE ON CURRENT DATA
1078          ;-----
1079
1080 003020 022777 001000 176154 .SCOPI: BIT    #SW09,@SWR   ;IS SW09=1(SET)?
1081 003026 001405          BEQ    1$         ;BR IF NOT SET.
1082 003030 005737 001220      TST    LOCK
1083 003034 001402          BEQ    1$
1084 003036 013716 001220      MOV    LOCK,(SP) ;GOTO THE ADDRESS IN LOCK.
1085 003042 000002          1$:  RTI           ;GO BACK.
1086
1087          ;TELETYPE OUTPUT ROUTINE
1088          ;-----
1089
1090 003044 010546          .TYPE: MOV    R5,-(SP) ;SAVE R5 ON THE STACK.
1091 003046 017605 000002      MOV    @2(SP),R5 ;GET ADDRESS OF MESSAGE.
1092 003052 062766 000002 000002 ADD    #2,2(SP)   ;POP OVER ADDRESS.
1093 003060 032777 010000 176114 1$:  BIT    #SW12,@SWR ;INHIBIT ALL PRINT OUT??
1094 003066 001012          BNE    3$         ;BR IF NO PRINT OUT WANTED (SW12=1)
1095 003070 105715          TSTB  (R5)       ;IS NUMBER MINUS? (MSB=1(BIT?))
1096 003072 100002          BPL    2$         ;BR IF NUMBER IS PLUS
1097 003074 104402 005104      TYPE  ,MCRLF     ;TYPE A CR/LF!
1098 003100 105777 176104      2$:  TSTB  @TPCSR   ;TTY READY?
1099 003104 100375          BPL    2$         ;BR IF NO.
1100 003106 112577 176100      MOVB  (R5)+,@TPDBR ;PRINT CURRENT CHAR.
1101 003112 001362          BNE    1$         ;IF NOT ZERO KEEP PRINTING!
1102 003114 012605          3$:  MOV    (SP)+,R5 ;END OF OUTPUT. RESTORE R5

```

```

1103 003116 000002          RTI          ;GO HOME
1104          :-----
1105
1106 003120 010346          .INSTR: MOV      R3, -(SP)          ;SAVE R3 ON STACK
1107 003122 010446          MOV      R4, -(SP)          ;SAVE R4 ON STACK
1108 003124 017637 000004 003142  MOV      @4(SP), .MSG
1109 003132 062766 000002 000004  ADD      #2, 4(SP)
1110 003140 104402          .INST1: TYPE
1111 003142 000000          .MSG: 0
1112 003144 012704 005520          MOV      #INBUF, R4
1113 003150 012703 000007          MOV      #7, R3
1114 003154 105777 176024          1$: TSTB    @TKCSR
1115 003160 100375          BPL      1$
1116 003162 117714 176020          MOVB    @TKDBR, (R4)
1117 003166 142714 000200          BICB    #200, (R4)
1118 003172 122427 000015          CMPB    (R4)+, #15
1119 003176 001417          BEQ     INSTR2
1120 003200 105777 176004          2$: TSTB    @TPCSR
1121 003204 100375          BPL      2$
1122 003206 017777 175774 175776          MOV      @TKDBR, @TPDBR
1123 003214 005303          DEC     R3
1124 003216 001356          BNE     1$
1125 003220 012604          MOV     (SP)+, R4
1126 003222 012503          MOV     (SP)+, R3
1127 003224 104402 005100          .INSTE: TYPE  MQM
1128 003230 010346          MOV     R3, -(SP)
1129 003232 010446          MOV     R4, -(SP)
1130 003234 000741          BR      .INST1
1131 003236 012604          INSTR2: MOV    (SP)+, R4          ;RESTORE R4
1132 003240 012603          MOV    (SP)+, R3          ;RESTORE R3
1133 003242 000002          RTI
1134
1135          ;CONVERT ASCII STRING TO OCTAL
1136          :-----
1137
1138 003244 010546          .PARAM: MOV     R5, -(SP)
1139 003246 010446          MOV     R4, -(SP)
1140 003250 016605 000004          MOV     4(SP), R5
1141 003254 012537 003434          MOV     (R5)+, LOLIM
1142 003260 012537 003436          MOV     (R5)+, HILIM
1143 003264 012537 003440          MOV     (R5)+, DEVADR
1144 003270 112537 003442          MOVB    (R5)+, LOBITS
1145 003274 112537 003443          MOVB    (R5)+, ADRCNT
1146 003300 010566 000004          MOV     R5, 4(SP)
1147 003304 005005          PARAM1: CLR    R5
1148 003306 012704 005520          MOV     #INBUF, R4
1149 003312 122714 000015          CMPB    #15, (R4)
1150 003316 001420          BEQ     PARERR
1151 003320 121427 000060          1$: CMPB    (R4), #60
1152 003324 002415          BLT     PARERR
1153 003326 121427 000067          CMPB    (R4), #67
1154 003332 003012          BGT     PARERR
1155 003334 142714 000060          BICB    #60, (R4)
1156 003340 152405          BISB    (R4)+, R5
1157 003342 122714 000015          CMPB    #15, (R4)
1158 003346 001406          BEQ     LIMITS

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 DZDVEB.P11 GENERAL UTILITIES (TYPE OUT, ERROR, SCOPE, ETC.)

```

1159 003350 006305          ASL      R5
1160 003352 006305          ASL      R5
1161 003354 006305          ASL      R5
1162 003356 000760          BR       1$
1163 003260 104404          PARERR: INSTER
1164 003362 000750          BR       PARAM1
1165
1166          ;TEST TO SEE IF NUMBER IS WITHIN LIMITS
1167          ;-----
1168
1169 003364 020537 003436      LIMITS: CMP      R5, HILIM
1170 003370 101373          BHI      PARERR
1171 003372 020537 003434      CMP      R5, LOLIM
1172 003376 103770          BLO      PARERR
1173 003400 133705 003442      BITB     LOBITS, R5
1174 003404 001365          BNE      PARERR
1175
1176          ;STORE NUMBER AT SPECIFIED ADDRESS
1177
1178 003406 013704 003440      1$:     MOV      DEVADR, R4
1179 003412 010524          MOV      R5, (R4)+
1180 003414 062705 000002      ADD      #2, R5
1181 003420 105337 003443      DECB     ADRCNT
1182 003424 001372          BNE      1$
1183 003426 012604          MOV      (SP)+, R4
1184 003430 012605          MOV      (SP)+, R5
1185 003432 000002          RTI
1186 003434 000000      LOLIM:  0
1187 003436 000000      HILIM:  0
1188 003440 000000      DEVADR: 0
1189 003442 000000      LOBITS: 0
1190          ADRCNT=LOBITS+1
1191
1192          ;SAVE PC OF TEST THAT FAILED AND R0-R5
1193          ;-----
1194
1195 003444 016637 000004 001276 .SAV05: MOV      4(SP), SAVPC      ;SAVE R7 (PC)
1196
1197          ;SAVE R0-R5
1198
1199 003452 010537 001272      SV05:  MOV      R5, SAVR5      ;SAVE R5
1200 003456 010437 001270      MOV      R4, SAVR4      ;SAVE R4
1201 003462 010337 001266      MOV      R3, SAVR3      ;SAVE R3
1202 003466 010237 001264      MOV      R2, SAVR2      ;SAVE R2
1203 003472 010137 001262      MOV      R1, SAVR1      ;SAVE R1
1204 003476 010037 001260      MOV      R0, SAVR0      ;SAVE R0
1205 003502 000002          RTI                      ;LEAVE.
1206
1207          ;RESTORE R0-R5
1208
1209 003504 013700 001260      .RES05: MOV      SAVR0, R0      ;RESTORE R0
1210 003510 013701 001262      MOV      SAVR1, R1      ;RESTORE R1
1211 003514 013702 001264      MOV      SAVR2, R2      ;RESTORE R2
1212 003520 013703 001266      MOV      SAVR3, R3      ;RESTORE R3
1213 003524 013704 001270      MOV      SAVR4, R4      ;RESTORE R4
1214 003530 013705 001272      MOV      SAVR5, R5      ;RESTORE R5

```

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003534 000002
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003745 000002
003746 000002

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```

RTI ;LEAVE
:CONVERT OCTAL NUMBER TO ASCII AND OUTPUT TO TELEPRINTER
-----

```

```

.CONVR: TYPE MCRLF
.CNVRT: MOV R0, -(SP)
MOV R1, -(SP)
MOV R3, -(SP)
MOV R4, -(SP)
MOV R5, -(SP)
MOV @12(SP), R1
ADD #2, 12(SP)
MOV (R1)+, WDCNT
15: MOV B (R1)+, CHRCNT
MOV B (R1)+, SPACNT
MOV @2(R1)+, BINWRD
25: MOV BINWRD, R4
MOV B CHRCNT, R5
MOV #TEMP, R0
35: MOV R4, R3
BIC #177770, R3
ADD #C60, R3
MOV B R3, R0+
CLC
RJR R4
CLC
RJR R4
CLC
RJR R4
DEC R5
45: MOV #MDATA, R3
MOV B -(R0), (R3)+
DECB CHRCNT
BNE 45
15TB SPACNT
55: MOV #040, (R3)+
DECB SPACNT
BNE 55
65: CLRB (R3)
TYPE MDATA
DEC WDCNT
BNE 15
MOV (SP)+, R5
MOV (SP)+, R4
MOV (SP)+, R3
MOV (SP)+, R1
MOV (SP)+, R0
RTI
WDCNT: 0
CHRCNT: 0
SPACNT=CHRCNT+1
BINWRD: 0

```



```

13275 004202 001402 BEQ 1$
13276 004204 104402 005400 TYPE .MASTEK
13277 004210 104402 005366 1$: TYPE .MTSTN
13278 004214 104411 004374 CNVRT .XTSTN :SHOW IT
13279 004220 104402 005454 TYPE .MERRPC :TYPE PC.
13280 004224 104411 004366 CNVRT .ERTABD :SHOW IT
13281 004230 104402 005104 TYPE .MCRLF :GIVE A CR/LF
13282 004234 112737 177777 001311 MOVB #1,ERRFLG :NO MORE HEADER UNLESS NO DATA TABLE.
13283 004242 005737 004252 TST ERMSG :IS THERE AN ERROR MESSAGE?
13284 004246 001402 BEQ WRKO.FM :BR IF NO.
13285 004250 104402 TYPE :TYPE
13286 004252 000000 ERMSG: 0 :ERROR MESSAGE
13287 004254 WRKO.FM: :
13288 004254 005737 004264 TST DATAHD :DATA HEADER?
13289 004260 001402 BEQ TYPDAT :BR IF NO
13290 004262 104402 TYPE :TYPE
13291 004264 000000 DATAHD: 0 :DATA HEADER
13292 004266 005737 004276 TYPDAT: TST DATASP :DATA TABLE?
13293 004272 001402 BEQ RESREG :BR IF NO.
13294 004274 104410 CNVRT :SHOW
13295 004276 000000 DATASP: 0 :DATA TABLE
13296 004300 104407 RESREG: RESOS :RESTORE PROC REGISTERS
13297 004302 005777 174674 HALTS: TST #SWR :HALT ON ERROR?
13298 004306 100005 BPL EXITER :BR IF NO HALT ON ERROR
13299 004310 010046 PUSHRO :SAVE RO
13300 004312 016600 MOV 2(SP),RO :SHOW ERROR PC IN DATA LIGHTS
13301 004316 000000 HALT :HALT
13302 004320 012600 POPRO :GET RO
13303 004322 005237 001232 EXITER: INC ERRCNT :UPDATE ERROR COUNT
13304 004326 032777 000400 174646 BIT #SWOB,#SWR :GOTO TOP OF TEST?
13305 004334 001007 BNE 1$ :BR IF YES
13306 004336 032777 002000 174636 BIT #SWID,#SWR :GOTO NEXT TEST?
13307 004344 001407 BEQ 2$ :BR IF NO
13308 004346 013737 001216 001214 MOV NEXT,RETURN :SET FOR NEXT TEST
13309 004354 012706 001200 1$: MOV #STACK,SP :RESET SP
13310 004360 000177 174630 JMP #RETURN :GOTO SPECIFIED TEST
13311 004364 000502 2$: RTI :RETURN
13312 004366 000001 ERTABD: 1
13313 004370 006 002 .BYTE 6,2
13314 004372 001276 SAVPC
13315 004374 000001 XTSTN: 1
13316 004376 003 002 .BYTE 3,2
13317 004400 001226 TSTNO :ENTER HERE ON POWER FAILURE
13318 -----
13319
13320 004402 .PFAIL:
13321 004402 012737 004414 000024 MOV #RESTART,24 :SET UP FOR POWER UP TRAP
13322 004410 000000 HALT :HALT ON POWER DOWN NORMAL
13323 004412 000777 BR .
13324
13325 :PROCESSOR WILL TRAP HERE WHEN POWER IS RESTORED
13326
13327 004414 RESTAR:
13328 004414 012737 004402 000024 MOV #.PFAIL,24 :SET UP FOR POWER FAILURE

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```

00000000 00000000 012706 001200 MOV #STACK,SP :RESET THE STACK POINTER
00000000 00000000 005037 005562 CLR TEMP :READY FOR TIMER
00000000 00000000 005237 005562 INC TEMP :PLUS ONE TO THE TIMER!
00000000 00000000 001375 BNE -4 :BR IF MORE TO GO
00000000 00000000 104402 005107 TYPE .MPFAIL :TYPE THE MESSAGE
00000000 00000000 104411 004470 CNVRT .PFTAB :TELL WHAT TEST TO RETURN TO.
00000000 00000000 105037 001311 CLR ERRFLG :START CLEAN
00000000 00000000 005037 001234 CLR LSTERR :.....
00000000 00000000 104412 MSTCLR :START CLEAN UP OF DEVICE
00000000 00000000 104413 ROMCLR :CLEAR IT ALL!
00000000 00000000 000177 174564 JMP @RETURN :START DOING THAT TEST AGAIN.
00000000 00000000 000001 002 FFTAB: 1
:BYTE 2 2
TSTNO
.DELAY: MOV RO,-(SP)
MOV R0,R0
DEC R0
BNE -2
MOV (SP)+,R0
RTI
IS: 30.
.RAMCLR:
MOV #MRESET,@DVSCR :ISSUE A MASTER CLEAR
MOV R1,-(SP) :SAVE R1 ON THE STACK
MOV R4,-(SP) :SAVE R4 ON THE STACK
MOV DVSR,R1 :GET SECONDARY SEL. REG.
MOV DVSR,R4 :GET SECONDARY REGISTER ACCESS REG.
IS: CLR (R4) :ZERO THE SECONDARY REGISTER.
ADD #C<BIT11+BIT10+BIT9+BIT8+BIT3+BIT2+BIT1+BIT0>,R1
IS:
MOV (SP)+,R4 :RESTORE R4
MOV (SP)+,R1 :RESTORE R1
RTI
.MSTCLR:
MOV #MRESET,@DVSCR :ISSUE MASTER CLEAR.
RTI
.ROMCLK:
BIS #BIT1,@DVSCR
RTI
.DATACLK:
MOV RO,-(SP)
CLR RO
IS: BIS #BIT8,@DVLCR
MOV @DVLCR,R0
RORB R0
BCC R0
INC R0
BNE IS
HLT 0
IS: MOV (SP)+,R0
RTI
IS: .BLKW 1

```

```

1439
1440 004640 032777 004000 174336 SERV.G: BIT #4000 @TKCSR :RX BUSY?
1441 004646 001374 :SERV.G :BR IF YES
1442 004650 017737 174326 005072 MOV @SWR,90$ :SAVE (SWR).
1443 004656 013777 005072 174316 1$: MOV 90$ @SWR :
1444 004664 104402 005052 TYPE .89$ :
1445 004670 104411 005064 CNVRT .88$ :
1446 004674 104402 005074 TYPE .91$ :
1447 004700 105777 174300 TSTB @TKCSR :WAIT FOR DONE.
1448 004704 100375 BPL -4 :
1449 004706 017746 174274 MOV @TKDBR,-(SP) :
1450 004712 042716 000200 BIC #BIT7,(SP) :
1451 004716 122726 000015 CMPB #15,(SP)+ :
1452 004722 001450 BEQ $$ :
1453 004724 005077 174252 CLR @SWR :
1454 004730 105777 174254 2$: TSTB @TPCSR :
1455 004734 100375 BPL -4 :
1456 004736 015677 177776 174246 MOV -2(SP),@TFDBR :
1457 004744 000241 CLC :
1458 004746 005177 174230 ROL @SWR :
1459 004752 006177 174224 ROL @SWR :
1460 004756 006177 174220 ROL @SWR :
1461 004762 103735 BCS 1$ :ERROR
1462 004764 026627 177776 000060 CMP -2(SP),#60 :
1463 004772 002731 BLT 1$ :
1464 004774 026627 177776 000067 CMP -2(SP),#67 :
1465 005002 003325 BGT 1$ :
1466 005004 042766 177770 177776 BIC #10(7),-2(SP) :
1467 005012 056677 177776 174162 BIS -2(SP),@SWR :
1468 005020 105777 174160 TSTB @TKCSR :
1469 005024 100375 BPL -4 :
1470 005026 017746 174154 MOV @TKDBR,-(SP) :
1471 005032 042716 000200 BIC #BIT7,(SP) :
1472 005036 122726 000015 CMPB #15,(SP)+ :
1473 005042 001332 BNE 2$ :
1474 005044 104402 005104 5$: TYPE MCRLF :
1475 005050 000207 RTS PC :
1476
1477 005052 020377 051450 051127 89$: .ASCIZ <377>? (SWR)=/?
1478 005060 036451 000057 :
1479
1480 .EVEN
1481 89$: 1
1482 005064 000001 :
1483 005066 006 000 .BYTE 6,0
1484 005070 005072 90$ :
1485 005072 000000 90$: .WORD 0
1486 005074 036457 000057 91$: .ASCIZ ?/?
1487
1488 .EVEN
1489 MQM: .ASCIZ / ?/
1490 MCRLF: .ASCIZ <15><12>
1491 005104 005015 000 MPFAIL: .ASCIZ <377>/PWR FAILED. RESTART AT TEST
1492 005107 377 053520 020122 MEPASS: .ASCIZ <377>/END PASS DZDVE-B
1493 005145 377 047105 020104 MR: .ASCIZ <377>/R/
1494 005171 377 000122 MERR2: .ASCIZ <377>/PROGRAM INDICATES NO DEVICES PRESENT.
1495 005174 050377 047522 051107 MERR3: .ASCIZ <377>/INSUFFICIENT DATA!
1496 005243 377 047111 052523 MTSTPC: .ASCIZ <377>/TEST PC-
1497 005267 377 042524 052123 MLOCK: .ASCIZ <377>/LOCK ON SELECTED TEST
1498 005301 377 047514 045503

```



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DVEB.P11 GENERAL UTILITIES (TYPE OUT,ERROR,SCOPE,ETC.)

005330  
005336  
005344  
005355  
005366  
005400  
005402  
005454  
005461  
  
005506  
005510  
005512  
005514  
005516  
  
005520  
005562  
005524

051503  
042536  
040520  
051123  
042524  
000052  
051771  
041520  
377  
  
000002  
000006  
001246  
000006  
  
000000  
000006  
000003  
000003

035122  
035103  
051523  
051123  
052123  
047040  
  
052105  
020073  
040515  
  
000040  
000040  
051505  
051117  
047040  
  
051440  
000  
220120  
  
003  
002  
  
0  
0  
0

MCSRX: .ASCIZ  
MVECX: .ASCIZ  
MPASSX: .ASCIZ  
MERRX: .ASCIZ  
MTSTN: .ASCIZ  
MASTEK: .ASCIZ  
MNEW: .ASCIZ  
MERRPC: .ASCIZ  
XHEAD: .ASCIZ  
XSTAT0: 2  
  
TEMP1  
TEMP2  
  
TEMP:  
DATA:  
  
0  
0  
0

CSR:  
VEC:  
PASSES:  
ERRORS:  
TEST NO:  
  
\*  
(377) SET SWITCH REG TO DV11'S DESIRED ACTIVE.  
(377) MAP OF DV11 STATUS (377)  
  
6.3  
6.2  
  
:BUFFERS FOR INPUT-OUTPUT

(377) SET SWITCH REG TO DV11'S DESIRED ACTIVE.  
(377) MAP OF DV11 STATUS (377)



1557	006166	013737	001376	001	MOV	DVSRA,DVSFR	:SPEC. FUN. REG.
1558	006174	060037	001400		ADD	RO,DVSFR	:
1559	006200	013737	001400	001402	MOV	DVSFR,DVNSR	:NPR STAT. REG.
1560	006206	060037	001402		ADD	RO,DVNSR	:
1561	006212	013737	001402	001404	MOV	DVNSR,RESV16	:RESERVED REG
1562	006220	060037	001404		ADD	RO,RESV16	:
1563							
1564	006224	013737	001352	001354	MOV	DVRVEC,DVRLVL	:PTY LVL
1565	006232	060037	001354		ADD	RO,DVRLVL	:
1566	006236	013737	001354	001356	MOV	DVRLVL,DVTEC	:TX VEC
1567	006244	060037	001356		ADD	RO,DVTEC	:
1568	006250	013737	001356	001360	MOV	DVTEC,DVTLVL	:TX LVL
1569	006256	060037	001360		ADD	RO,DVTLVL	:
1570							
1571	006262	012700	001416		MOV	#L00.03,RO	:LOAD STAUS 00-03
1572	006266	012701	001406		MOV	#MASK.A,R1	:PREPARE MASK.
1573	006272	012702	001412		MOV	#CLK.A,R2	:PREPARE CLOCKS
1574	006276	004737	006516		JSR	PC,FIX.00	:GO AND CALCULATE CONFIGURATION.
1575							
1576	006302	012700	001420		MOV	#L04.07,RO	:LOAD STAUS 00-03
1577	006306	012701	001407		MOV	#MASK.B,R1	:PREPARE MASK.
1578	006312	012702	001413		MOV	#CLK.B,R2	:PREPARE CLOCKS
1579	006316	004737	006516		JSR	PC,FIX.00	:GO AND CALCULATE CONFIGURATION.
1580							
1581	006322	012700	001422		MOV	#L08.11,RO	:LOAD STAUS 00-03
1582	006326	012701	001410		MOV	#MASK.C,R1	:PREPARE MASK.
1583	006332	012702	001414		MOV	#CLK.C,R2	:PREPARE CLOCKS
1584	006336	004737	006516		JSR	PC,FIX.00	:GO AND CALCULATE CONFIGURATION.
1585							
1586	006342	012700	001424		MOV	#L12.15,RO	:LOAD STAUS 00-03
1587	006346	012701	001411		MOV	#MASK.D,R1	:PREPARE MASK.
1588	006352	012702	001415		MOV	#CLK.D,R2	:PREPARE CLOCKS
1589	006356	004737	006516		JSR	PC,FIX.00	:GO AND CALCULATE CONFIGURATION.
1590	006362	032777	000002	172612	BIT	#SW01,2SWR	
1591	006370	001445			BEQ	7\$	
1592	006372						
1593	006372	005737	000042		TST	2*42	
1594	006376	001042			BNE	7\$	
1595	006400	104402	005104		TYPE	.MORLF	
1596	006404	104403			INSTR		
1597	006406	005366			MTSTN		
1598	006410	104405			PARAM		
1599	006412	000001			1		
1600	006414	001000			1000		
1601	006416	001226			TSTNO		
1602	006420	000			0		
1603	006421	001			.BYTE		
1604	006422	012700	007306		MOV	#TST1,RO	
1605	006426	022710			CMP	(PC)+,(RO)	
1606	006430	012737			MOV	(PC)+,2(PC)+	
1607	006432	001015			BNE	6\$	
1608	006434	023760	001226	000002	CMP	TSTNO,2(RO)	
1609	006442	001011			BNE	6\$	
1610	006444	022760	001226	000004	CMP	#TSTNO,4,RO	
1611	006452	001005			BNE	6\$	
1612	006454	010037	001214		MOV	RO,RETURN	

```

1613 006460 104402 005104 TYPE MCRLF
1614 006464 000412 BR 5$
1615 006466 005720 6$: TST (R0)+
1616 006470 020027 020456 CMP RO, #TLAST+10
1617 006474 001354 BNE 5$
1618 006476 104402 005100 TYPE .MQM
1619 006502 000733 SR 4$
1620 006504 012737 007306 001214 7$: MOV #TST1, RETURN ;PREPARE RETURN ADDRESS
1621 006512 000177 172476 8$: JMP @RETURN ;GO START TESTING.
1622
1623 006516 011003 FIX.00: MOV (R0), R3 ;GET PARAMETERS.
1624 006520 042703 176377 BIC #1C<1400>, R3 ;CLEAR JUNK.
1625 006524 005703 TST R3 ;TEST FOR EIGHT BITS.
1626 006526 001004 BNE 1$ ;BR IF NOT 8 BITS.
1627 006530 105011 CLRB (R1) ;SET
1628 006532 112712 000010 MOVB #6., (R2) ;
1629 006536 000424 BR 4$ ;
1630 006540 022703 000400 1$: CMP #400, R3 ;CHECK FOR SEVEN BITS.
1631 006544 001005 BNE 2$ ;BR IF NOT 7 BITS.
1632 006546 112711 000200 MOVB #200, (R1) ;
1633 006552 112712 000007 MOVB #7, (R2) ;
1634 006556 000414 BR 4$ ;
1635 006560 022703 001000 2$: CMP #1000, R3 ;CHECK FOR SIX BITS.
1636 006564 001005 BNE 3$ ;BR IF NOT SIX BITS.
1637 006566 112711 000300 MOVB #300, (R1) ;
1638 006572 112712 000006 MOVB #6, (R2) ;
1639 006576 000404 BR 4$ ;
1640 006600 112711 000340 3$: MOVB #340, (R1) ;IF NONE OF THE ABOVE; MUST BE 5 BITS.
1641 006604 112712 000005 MOVB #5, (R2) ;
1642 006610 032710 040000 4$: BIT #PARBIT, (R0) ;PARITY ENABLED?
1643 006614 001401 BEQ 5$ ;IF =0; THEN NO PARITY.
1644 006616 105212 INCB (R2) ;PLUS ONE TO THE CLOCK!
1645 006620 000207 5$: RTS PC ;
1646
1647 ;*ROUTINE USED TO "AUTO SIZE" THE DV11
1648 ;*CSR AND VECTOR.
1649 ;*NOTE: THE CSR MAY BE ANY WHERE IN THE FLOATING
1650 ;* ADDRESS RANGE (175000:175400)
1651 ;* AND THE VECTOR MAY BE ANY WHERE IN THE
1652 ;* FLOATING VECTOR RANGE (300:770)
1653 ;*
1654
1655 AUTO.SIZE:
1656 006622 000005 RESET ;INSURE A BUS INIT.
1657 006624 012702 001500 CSRMAP: MOV #DV.MAP, R2 ;LOAD MAP POINTER.
1658 006630 005022 1$: CLR (R2)+ ;ZERO ENTIRE MAP
1659 006632 022702 001740 CMP #DV.END, R2 ;ALL DONE?
1660 006636 001374 BNE 1$ ;BR IF NO
1661 006640 105037 001301 CLRB DVNUM ;SET OCTAL NUMBER OF DV11'S TO 0
1662 006644 012702 001500 MOV #DV.MAP, R2
1663 006650 012701 175000 MOV #175000, R1 ;SET FOR FIRST ADDRESS TO BE TESTED
1664 006654 012737 007074 000004 MOV #6$, @#4 ;SET FOR NON-EXISTANT DEVICE TIME OUT
1665 006662 005711 2$: TST (R1) ;IF DV11 DVSCR S/B 0
1666 006664 001037 BNE 3$ ;IF NO DEV ; TRAP TO 4. IF NO BIT 8 THEN NO DV11
1667 006666 022761 177777 000012 CMP #177777, 12(R1) ;IF DV11 THEN DVSCR S/B ALL 1'S ON INIT!
1668 006674 001033 BNE 3$ ;BR IF NOT DV11
  
```

# K03

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 DDVEB.P11 GENERAL UTILITIES (TYPE OUT,ERROR,SCOPE,ETC.)

```

1669 006676 005761 000016      TST      16(R1)      ;IF DV11 THEN RESV16 S/8 ALL 0'S
1670 006702 001030      BNE      3$        ;BR IF NOT DV11
1671      ;AT THIS POINT IT IS ASSUMED THAT R1 HOLDS A DV11 CSR ADDRESS.
1672 006704 010122      MOV      R1,(R2)+  ;STORE CSR IN CORE TABLE.
1673 006706 005722      TST      (R2)+     ;POP OVER VECTOR STORE AREA
1674 006710 052722 000226      BIS      #226,(R2)+ ;SET LINE CARD 1 STAT AND SYNC
1675 006714 052722 000062      BIS      #62,(R2)+ ;
1676 006720 052722 000226      BIS      #226,(R2)+ ;SET LINE CARD 2 STAT AND SYNC
1677 006724 052722 000062      BIS      #62,(R2)+ ;
1678 006730 052722 000226      BIS      #226,(R2)+ ;SET LINE CARD 3 STAT AND SYNC
1679 006734 052722 000062      BIS      #62,(R2)+ ;
1680 006740 052722 000226      BIS      #226,(R2)+ ;SET LINE CARD 4 STAT AND SYNC
1681 006744 052722 000062      BIS      #62,(R2)+ ;
1682 006750 105237 001301      INCB     DVNUM      ;UPDATE DEVICE COUNTER
1683 006754 122737 000010 001301      CMPB     #10,DVNUM  ;ARE MAX. NO. OF DEV FOUND?
1684 006762 001405      BEQ      100$     ;YES DON'T LOOK FOR ANY MORE.
1685 006764 062701 000010 3$:      ADD      #10,R1    ;UPDATE CSR POINTER ADDRESS
1686 006770 022731 175400      CMP      #175400,R1
1687 006774 001332      BNE      2$        ;BR IF MORE ADDRESS TO CHECK.
1688 006776 012722 177777 100$:     MOV      #177777,(R2)+ ;TERMINATER.
1689 007002 105037 001300      CLRB     DVACTV
1690 007006 105737 001301      TSTB     DVNUM      ;WERE ANY DV11'S FOUND AT ALL?
1691 007012 001423      BEQ      5$        ;ERROR AUTO SIZER FOUND NO DV11'S IN THIS SYS.
1692 007014 113701 001301      MOVB     DVNUM,R1
1693 007020 110137 001303      MOVB     R1,SAVNUM  ;SAVE NUMBER OF DEVICES
1694 007024 000241 4$:      CLC
1695 007026 106137 001300      ROLB     DVACTV     ;GENERATE ACTIVE REGISTER OF DEVICES.
1696 007032 105237 001300      INCB     DVACTV     ;SET THE BIT
1697 007036 005301      DEC      R1
1698 007040 001371      BNE      4$        ;BR IF MORE TO GENERATE
1699 007042 012737 000006 000004 000004+ ;RESTORE TRAP VECTOR
1700 007050 113737 001300 001302      MOVB     #6,D#4
1701 007056 000137 007102      MOVB     DVACTV,SAVACT ;SAVE ACTIVE REGISTER
1702 007062 104402 005174 5$:      JMP      VECMAP    ;GO FIND THE VECTOR NOW.
1703 007066 005000      MERR2
1704 007070 000000      CLR      RC        ;NOTIFY OPR THAT NO DV11'S FOUND.
1705 007072 000776      HALT     ;MAKE DATA LIGHTS ZERO
1706 007074 012716 006764 6$:      BR       .-2       ;STOP THE SHOW
1707 007100 000002      BR       #3$(SP)  ;DISABLE CONT. SW.
1708      ;ENTERED BY NON-EXISTANT TIME-OUT.
1709 007102 012737 000340 000022 VECMAP: MOV      #340,D#22  ;RETURN TO MAINSTREAM
1710 007110 012737 007232 000020      MOV      #4$,D#20  ;SET IOT TRAP Prio TO 7
1711 007116 012702 001500      MOV      #DV_MAP,R2 ;SET IOT TRAP VECTOR
1712 007122 012700 000300      MOV      #300,RO    ;SET SOFTWARE POINTER
1713 007126 012701 000302      MOV      #302,R1    ;FLOATING VECTORS START HERE.
1714 007132 010120 1$:      MOV      R1,(R0)+  ;PC OF IOT INSTR.
1715 007134 012721 000004      MOV      #4,(R1)+  ;START FILLING VECTOR AREA
1716 007140 022021      MOV      #4,(R1)+  ;WITH .+2; IOT
1717 007142 020127 001000      CMP      (R0)+(R1)+ ;ADD 2 TO RO +R1
1718 007146 101771      CMP      R1,#1000
1719 007150 113737 001300 001246 1$:      BLOS     1$        ;BR IF MORE TO FILL
1720 007156 006037 001246 2$:      MOVB     DVACTV,TEMP1 ;STORE TEMPORALLY
1721 007162 103034      ROR      TEMP1     ;BRING OUT A BIT
1722 007164 005037 177776      BCC      5$        ;BR IF ALL DONE
1723 007170 012772 001300 000000      CLR      PS        ;ZERO CPU Prio
1724 007176 005000      MOV      #BIT9+BIT7+BIT6,D(R2)
1724      CLR      RO      ;ATTEMPT TO FORCE AN INTERLPT

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*****
* THIS "TEST 1" IS NOT ACTUALLY A TEST.
* IT IS USED TO GET USERS INPUTS FOR WHICH LINE(S) ARE TO BE
* EXERCISED. THE PROGRAM WILL TYPE OUT:
* (A) H325
* (B) H861
* TYPE "A" "OR" "B"
*
* THE H325 TURN AROUND IS USED FOR THE SINGLE LINE
* TURN AROUND AT THE DISTRIBUTION PANEL OR
* AT THE END OF THE MODEM CABLE.
* THE H861 TURN AROUND IS USED FOR THE 16 LINE TURN AROUND.
* IF THE H325 WAS SELECTED (A) THE FOLLOWING WILL BE TYPED
* IF SW06=0:
* SELECT LINE(S): XXXXXXXXXXXXXXXX
*
* THE FIRST "X" REPRESENTS LINE 15 AND EACH "X" IS THE
* NEXT LOWER LINE TILL THE LAST "X" IS LINE 0. TYPE
* A "1" OR A "0" UNDER THE APPROPRIATE "X"(LINE)
* TO EITHER SELECT(1) OR NOT TEST(0) EACH LINE.
* AFTER ALL 1'S AND 0'S ARE TYPED; TYPE A <CR>.
* THE PROGRAM WILL TYPE OUT IN OCTAL THE LINES YOU
* HAVE SELECTED; AND THE PROGRAM WILL BEGIN RUNNING
* THE HIGHEST SELECTED LINE THROUGH *ALL* TESTS THEN
* UPDATING TO THE NEXT LOWEST LINE TILL ALL SELECTED
* LINES ARE DONE. THEN THE PROGRAM WILL TYPE AN
* "END" CHAR. PLEASE READ THE SECTION ON PASS COMPLETE
* IN DOCUMENT.
* IF THE H325 IS SELECTED AND SW06=1 THE FOLLOWING WILL BE TYPED:
* SINGLE LINE:
* THE USER MUST THEN TYPE IN A SINGLE LINE HE DESIRES (00-17) -OCTAL-
* END PASS IS THE SAME.
* REGARDLESS OF WHICH CONNECTOR WAS SELECTED; THE
* THE LAST QUESTION IS:
* MODEM VECTOR:
* (THIS WILL BE ASKED ONLY AT THE INIATL START OF PROGRAM
* OR WHEN A DIFFERENT DV11 IN THE SYSTEM IS UNDER TEST)
* TYPE IN THE VECTOR OF THE MODEM CONTROL(300:774).
* THE CSR(MC.CSR) IS ASSUMED TO BE =DVSCR+20.
* NOTE: IF CABLE TESTS ARE TO BE DONE ON OTHER
* DV11'S IN SYSTEM; SELECT THEM BY USING SW00 AS DESCRIBED
* IN THE DOCUMENTATION.
* UNLESS LOCATION 42 IS NON-ZERO IN WHICH CASE THE PROGRAM
* ASSUMES ITS UNDER ACT-11 MONITOR. THE PROGRAM WILL
* CYCLE THROUGH ALL DV11S AND MODEM CONTROL *HOWEVER*
* THE RESTRICTIONS ARE:
* ***ALL*** MODEM VECTORS MUST BE AT 300
* ***ALL*** TURN AROUNDS MUST BE H861.
* "LONG END PASS" WILL BE GIVEN AT END OF LARGE END TO
* INDICATE DEVICES TESTED. PASSES TYPED IN THIS
* MESSAGE DO NOT INDICATE PASSES BUT RATHER THE
* NUMBER OF FULL PASSES THROUGH MULTIPLE DEVICES.
* !LARGE END AND TYPE OUT MAY BE INHIBITED BY SW12!
*****

```



TEST 1

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000001 001226
000002 001215
000003 007276
000004 007276
000005 007300
000006 007300
000007 000050
000008 000062
000009 171504
000010 177776
000011 000042
023247
023334
000101 001272
000377 007256
000102 001272
000256 007270
000001 007270
000100 171504
0001421
0007500 022240
0007504 104403
0007506 000000
0007510 000017
0007512 007260
0007514 000
0007516 012737 000001 007270
0007524 005337 007260
0007530 100502
0007532 000241
0007534 006137 007270
0007540 000771
0007542 104402 022125
0007546 013737 007270 001252
0007554 005037 007270
0007560 105777 171420
0007564 100375
0007566 017700 171414
0007572 010077 171414
0007576 042700 177600
0007602 022700 000123
0007606 001004
0007610 013737 001252 007270
0007616 000415
0007620 022700 000015
0007624 001412
0007626 022700 000060

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TEST1: MOV #1,TSTNO
MOV #TST2,NEXT
CLR PS
MOV DVSCR,MC,CSR
ADD #20,MC,CSR
MOV MC,CSR,MC,LSR
ADD #2,MC,LSR
MOV #KBISR,2#60
MOV #340,2#62
MOV #100,2TKCSR
MOV #340,PS
TST 2#42
BNE 44$
1$: TYPE .MTURN
ISR PC,TKRDY
CMPB #101,SAVRS
BNE 70$
MOV #377,TURFLG
BR 71$
70$: CMPB #102,SAVRS
BNE 1$
44$: CLR TURFLG
MOV #1,SELECT
BR 68$
71$: BIT #SW06,2SWR
BEQ 72$
MAP18= INSTR .MSING
PARAM
CC
17
LINE
.BYTE 0,1
MOV #1,SELECT
74$: DEC LINE
BNE 68$
CLC
ROL
BR 74$
72$: TYPE .MSEL
MOV SELECT,TEMP3
CLR SELECT
2$: TSTB 2TKCSR
BPL 2$
MOV 2TKOBR,RO
MOV RO,2TPOBR
BIC #1<17>,RO
CMP #123,RO
BNE .+12
MOV TEMP3,SELECT
BR 4$
CMP #15,RO
BEQ 4$
CMP #60,RO

```

```

: CLEAR CPL STATUS
: GET MODEM CSR
: IT HAS TO BE 2018) MORE THAN DVSCR.
: GET MODEM LSR
: MUST BE 2 MORE THAN CSR
: SET KEYBOARD INTERRUPT VEC
: SET LEV TO 7
: SET INTERRUPT ENABLE
: LOCK OUT TTY

: ASK FOR LINES
: GET PREVIOUS LINE SELECT
: MAKE IT 0
: READY?
: BR IF NO
: READ CHAR
: ECHO CHAR
: STRIP ALL BUT DATA
: WAS IT "SAME"?
: BR IF NO
: RESTORE PREVIOUS LINES SELECTED
: GO ON
: WAS IT "<CR>"
: BR IF YES
: WAS IT "0"

```



000142	000177	171046	
000146	012737	010326	007262
000154	117737	177102	007266
000162	005237	007262	45:
000166	117737	177070	007264
000174	005237	007262	
000200	013737	007270	35:
000206	012737	000020	007260
000214	000722		
000216	012737	010766	25:
000224	013737	001214	001216
000232	005046		
000234	012746	010270	
000240	032777	004000	170735
000246	001374		
000250	017746	170732	
000254	042716	000200	
000260	122726	000001	
000264	001403		
000266	022626		
000270	000177	170720	55:
000274	010046		
000276	017700	170704	
000302	042700	177600	
000306	022700	000001	
000312	001002		
000314	012766	007500	000002
000322	012600		
000324	000002		
000326	001		
000328	001		
000330	001		
000332	001		
000334	001		
000336	001		
000338	001		
000340	001		
000342	001		
000344	001		
000346	001		
000348	001		
000350	001		
000352	001		
000354	001		
000356	001		
000358	001		
000360	001		
000362	001		
000364	001		
000366	001		
000368	001		
000370	001		
000372	001		
000374	001		
000376	001		
000378	001		
000380	001		
000382	001		
000384	001		
000386	001		
000388	001		
000390	001		
000392	001		
000394	001		
000396	001		
000398	001		
000400	001		

```

JMP      JRETURN
MOV      #TABLE, POINTER
MOVB    JPOINTER, COUNT
INC      POINTER
MOVB    JPOINTER, CHAR
INC      POINTER
MOV      SELECT, EXERCISE
MOV      #20, LINE
BR       TESTER
MOV      #TST2, RETURN
MOV      RETURN, NEXT
CLR      -(SP)
MOV      #55, -(SP)
BIT      #BIT11, JTKCSR
BNE     -E
MOV      JTKCSR, -(SP)
BIC     #BIT7, (SP)
CMPB    #1, (SP)+
SEQ     #BISR
CMP     (SP)+, (SP)+
JMP      JRETURN

KBISR:  MOV      RO, -(SP)
        MOV      JTKCSR, RO
        BIC     #1<17>, RO
        CMP     #1, RO
        BNE     #S
        MOV      #MAR18, 2(SP)
        MOV      (SP)+, RO
        RTI

TABLE:  .BYTE  01, 15, 2, 12
        .BYTE  01, 40, 10, 105, 4, 40, 2, 116, 6, 40, 2, 116, 4, 40, 9, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 10, 105, 4, 40, 2, 116, 6, 40, 2, 116, 4, 40, 9, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 2, 105, 12, 40, 2, 116, 6, 40, 2, 116, 4, 40, 2, 104, 6, 40, 2, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 2, 105, 12, 40, 2, 116, 6, 40, 2, 116, 4, 40, 2, 104, 5, 40, 2, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 2, 105, 12, 40, 4, 116, 4, 40, 2, 116, 4, 40, 2, 104, 6, 40, 2, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 2, 105, 12, 40, 4, 116, 4, 40, 2, 116, 4, 40, 2, 104, 6, 40, 2, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 8, 105, 6, 40, 2, 116, 2, 40, 2, 116, 2, 40, 2, 116, 4, 40, 2, 104, 6, 40, 2, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 8, 105, 6, 40, 2, 116, 2, 40, 2, 116, 2, 40, 2, 116, 4, 40, 2, 104, 6, 40, 2, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 2, 105, 12, 40, 2, 116, 4, 40, 4, 116, 4, 40, 2, 104, 6, 40, 2, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 2, 105, 12, 40, 2, 116, 4, 40, 4, 116, 4, 40, 2, 104, 6, 40, 2, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 10, 105, 4, 40, 2, 116, 6, 40, 2, 116, 4, 40, 9, 104
        .BYTE  01, 15, 1, 12
        .BYTE  01, 40, 10, 105, 4, 40, 2, 116, 6, 40, 2, 116, 4, 40, 9, 104

```

```

:SET FOR FAKE INTR
:SET FAKE PC OF INTR
:TTY ACTIVE?
:YES WAIT TILL DONE.

:CLEAR PARITY
:WAS IA (CHANGE LINES) HIT?
:BR IF YES
:BR TO #BISR NOT TAKEN
:POP FAKE INTR OFF STACK

```

E04

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DUAL DEVICE DIAGNOSTICS.

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01076  
01076

001  
000  
00766

015  
000

001  
000

.BYTE 1.15, 1.12  
.BYTE 0.0.0  
.EVEN

..

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\*\*\*\*\* TEST 2 \*\*\*\*\*  
:INITIALIZATION CHECK  
:VERIFY THAT CONTROL STATUS REGISTER AND LINE STATUS  
:REGISTER WERE CLEARED BY INITIALIZE  
\*\*\*\*\*

: TEST 2

010766 012737 000002 001226  
010774 012737 011124 001216  
011002 105777 170202  
011006 100375  
011010 000005  
011012 005005  
011014 052777 000100 170162  
011022 012737 011112 000004  
011030 012702 000010  
011034 027777 170144 17014E  
011042 027777 170136 170134  
011050 005302  
011052 001370  
011054 005200  
011056 013703 00727E  
011062 011304  
011064 001401  
011066 104002  
011070 013703 007300  
011074 011304  
011076 001401  
011100 104002  
011102 012737 000006 000004  
011110 104400  
011112 104005  
011114 012706 001200  
011120 000177 170070

TST2: MOV #2, TSTNO  
MOV #TST3, NEXT  
TSTB #ATPCSR :WAIT FOR TTY READY  
BPL #-4 :BR IF NOT READY  
RESET :INIT  
CLR R5  
BIS #100, #ATKCSR :SET TTY INTERRUPT ENABLE  
MOV #15, #R4 :SET FOR NON-EX. DEVICE.  
MOV #8, R2 :SET COUNTER  
55E: CMP #ATKCSR, #ATKCSR :WASTE TIME  
CMP #ATKCSR, #ATKCSR :WASTE TIME  
DEC R2 :DELAY DONE?  
BNE 65E :BR IF NO  
INC R0 :FLASH LIGHTS  
MOV MC.CSR, R3 :SET MC.CSR POINTER  
MOV (R3), R4 :READ REGISTER  
BEQ #-4  
HLT 2 :CONTROL STATUS NOT CLEARED. ERROR  
MOV MC.LSR, R3 :SET POINTER  
MOV (R3), R4 :READ MC.LSR  
BEQ #-4  
HLT 2 :LINE STATUS NOT CLEARED. ERROR  
MOV #6, #R4 :RESET TRAP CATCHER  
SCOPE :CHECK FOR LOOP  
15: HLT 5 :SHOULD NOT TRAP.  
MOV #STACK, SP  
JMP #RETURN

\*\*\*\*\* TEST 3 \*\*\*\*\*  
:VERIFY THAT "INTERUPT ENABLE" CAN BE  
:SET AND CLEARED.  
\*\*\*\*\*

: TEST 3

011124 012737 000003 001226  
011132 012737 011216 001216  
011140 013703 007276  
011144 012713 000100  
011150 011304  
011152 042704 177677  
011156 012705 000100  
011162 020504  
011164 001401  
011166 104002  
011170 042705 000100  
011174 042713 000100

TST3: MOV #3, TSTNO  
MOV #TST4, NEXT  
MOV MC.CSR, R3 :SET POINTER TO MC.CSR  
MOV #INTENA, (R3) :LOAD FUNCTION  
MOV (R3), R4 :READ RESULTS  
BIC #INTENA, R4 :MASK OFF ALL OTHER BITS.  
MOV #INTENA, R5 :MAKE R5=GOOD  
CMP R5, R4 :RESULTS OK?  
BEQ #-4 :BR IF YES  
HLT 2 :ERROR. R5=GOOD, R4=BAD, R3=REGISTER  
BIC #INTENA, R5  
BIC #INTENA, (R3) :CLEAR BIT

# H04

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00048	011200	011204		MOV	(R3), R4	: READ REGISTER
00049	011202	042704	177577	BIC	#1C<INTENA>, R4	: MASK OFF ALL OTHER BITS.
00050	011206	020504		CMP	R5, R4	: REGISTER OK?
00051	011210	001401		BEQ	.+4	: BR IF YES
00052	011212	104002		HLT	2	: BIT FAILED TO CLEAR
00053	011214	104400		SCOPE		: SCOPE TEST.

\*\*\*\*\* TEST 4 \*\*\*\*\*  
\*VERIFY THAT "DONE" CAN BE  
\*SET AND CLEARED.  
\*\*\*\*\*

## TEST 4

00054	011216	012737	000004	001226	TST4:	MOV	#4, TSTNO	
00055	011224	012737	011310	001216		MOV	#TST5, NEXT	
00056	011232	013703	007276			MOV	MC.CSR, R3	: SET POINTER TO MC.CSR
00057	011236	012713	000200			MOV	#DONE, (R3)	: LOAD FUNCTION
00058	011242	011304				MOV	(R3), R4	: READ RESULTS
00059	011244	042704	177577			BIC	#1C<DONE>, R4	: MASK OFF ALL OTHER BITS.
00060	011250	012705	000200			MOV	#DONE, R5	: MAKE R5=GOOD
00061	011254	020504				CMP	R5, R4	: RESULTS OK?
00062	011256	001401				BEQ	.+4	: BR IF YES
00063	011260	104002				HLT	2	: ERROR. R5=GOOD, R4=BAD, R3=REGISTER
00064	011262	042705	000200			BIC	#DONE, R5	
00065	011266	042713	000200			BIC	#DONE, (R3)	: CLEAR BIT
00066	011272	011304				MOV	(R3), R4	: READ REGISTER
00067	011274	042704	177577			BIC	#1C<DONE>, R4	: MASK OFF ALL OTHER BITS.
00068	011300	020504				CMP	R5, R4	: REGISTER OK?
00069	011302	001401				BEQ	.+4	: BR IF YES
00070	011304	104002				HLT	2	: BIT FAILED TO CLEAR
00071	011306	104400				SCOPE		: SCOPE TEST.

\*\*\*\*\* TEST 5 \*\*\*\*\*  
\*VERIFY THAT "MAINTENANCE MODE" CAN BE  
\*SET AND CLEARED.  
\*\*\*\*\*

## TEST 5

00072	011310	012737	000005	001226	TST5:	MOV	#5, TSTNO	
00073	011316	012737	011402	001216		MOV	#TST6, NEXT	
00074	011324	013703	007276			MOV	MC.CSR, R3	: SET POINTER TO MC.CSR
00075	011330	012713	001000			MOV	#MAINT, (R3)	: LOAD FUNCTION
00076	011334	011304				MOV	(R3), R4	: READ RESULTS
00077	011336	042704	176777			BIC	#1C<MAINT>, R4	: MASK OFF ALL OTHER BITS.
00078	011342	012705	001000			MOV	#MAINT, R5	: MAKE R5=GOOD
00079	011346	020504				CMP	R5, R4	: RESULTS OK?
00080	011350	001401				BEQ	.+4	: BR IF YES
00081	011352	104002				HLT	2	: ERROR. R5=GOOD, R4=BAD, R3=REGISTER
00082	011354	042705	001000			BIC	#MAINT, R5	
00083	011360	042713	001000			BIC	#MAINT, (R3)	: CLEAR BIT
00084	011364	011304				MOV	(R3), R4	: READ REGISTER
00085	011366	042704	176777			BIC	#1C<MAINT>, R4	: MASK OFF ALL OTHER BITS.

# I04

011372 020504  
011374 001401  
011376 104002  
011400 004400

CMP R5,R4 :REGISTER OK?  
BEQ +4 :BR IF YES  
HLT 2 :BIT FAILED TO CLEAR  
SCOPE :SCOPE TEST.

\*\*\*\*\* TEST 6 \*\*\*\*\*  
\*VERIFY THAT "SCAN ENABLE" CAN BE  
\*SET AND CLEARED.  
\*\*\*\*\*

## : TEST 6

011402 012737 000006 001226  
011410 012737 011474 001216  
011416 013703 007276  
011422 012713 000040  
011426 011304  
011430 042704 177737  
011434 012705 000040  
011440 020504  
011442 001401  
011444 104002  
011446 042705 000040  
011452 042713 000040  
011456 011304  
011460 042704 177737  
011464 020504  
011466 001401  
011470 104002  
011472 004400

TEST6: MOV #6,TSTNO  
MOV #TST7,NEXT  
MOV MC.CSR,R3 :SET POINTER TO MC.CSR  
MOV #SCNENA,(R3) :LOAD FUNCTION  
MOV (R3),R4 :READ RESULTS  
BIC #C<SCNENA>,R4 :MASK OFF ALL OTHER BITS.  
MOV #SCNENA,R5 ;MAKE R5=GOOD  
CMP R5,R4 :RESULTS OK?  
BEQ +4 :BR IF YES  
HLT 2 ;ERROR. R5=GOOD,R4=BAD,R3=REGISTER  
BIC #SCNENA,R5  
BIC #SCNENA,(R3) :CLEAR BIT  
MOV (R3),R4 :READ REGISTER  
BIC #C<SCNENA>,R4 :MASK OFF ALL OTHER BITS.  
CMP R5,R4 :REGISTER OK?  
BEQ +4 :BR IF YES  
HLT 2 :BIT FAILED TO CLEAR  
SCOPE :SCOPE TEST.



# J04

```

:***** TEST 7 *****
:*VERIFY THAT "BUSY" IS SET WHEN "SCAN ENABLE" IS SET
:*VERIFY THAT "BUSY" IS CLEARED WHEN "SCAN ENABLE" IS CLEARED
:*****
    
```

: TEST 7

1136 1137 1138 1139 1140 1141 1142 1143 1144 1145 1146 1147 1148 1149 1150 1151 1152 1153 1154 1155 1156 1157 1158 1159 1160 1161 1162 1163 1164 1165 1166 1167 1168 1169 1170 1171 1172 1173 1174 1175 1176 1177 1178 1179 1180 1181 1182 1183	011474 011502 011510 011514 011520 011522 011524 011530 011532 011534 011536 011542 011550 011556 011560 011562 011566 011570 011572 011574	012737 012737 013703 012713 011304 010405 052705 020504 001401 104002 042713 023737 023737 011304 010405 042705 020504 001401 104002 104400	000007 011576 007276 000040  000020   000040 000000 000000  000020   	001226 001216      000000 000000      	<pre> †ST7:  MOV      #7,TSTNO         MOV      #TST10,NEXT         MOV      MC.CSR,R3          ;SET REGISTER POINTER         MOV      #SCNENA,(R3)      ;SET SCAN ENABLE         MOV      (R3),R4           ;READ REGISTER         MOV      R4,R5             ;GET IMAGE         BIS      #BUSY,R5          ;SET BUSY BIT IN GOOD.         CMP      R5,R4             ;REGISTER OK?         BEQ      .+4         HLT      2                  ;BUSY NOT SET, ERROR         BIC      #SCNENA,(R3)      ;CLEAR SCAN ENABLE         CMP      0,0                ;GIVE BUSY A CHANCE TO CLEAR         CMP      0,0                ;WHEN ON A HOT ROD MACHINE (11/70)!         MOV      (R3),R4           ;READ MC.CSR         MOV      R4,R5             ;GET IMAGE         BIC      #BUSY,R5          ;CLEAR BUSY IN GOOD.         CMP      R5,R4             ;BUSY CLEARED?         BEQ      .+4         HLT      2                  ;BUSY NOT CLEARED, ERROR         SCOPE                      ;CHECK FOR LOOP, ITERATIONS     </pre>
--	--	--	--	--	---

```

:***** TEST 10 *****
:*VERIFY THAT SETTING "DONE" DOES NOT CAUSE AN
:*INTERRUPT IF "INTERRUPT ENABLE" IS CLEARED.
:*****
    
```

: TEST 10

1171 1172 1173 1174 1175 1176 1177 1178 1179 1180 1181 1182 1183	011576 011604 011612 011620 011624 011632 011640 011646 011652 011654 011656 011660 011662	012737 012737 012737 005077 012777 012777 052777 005037 000240 000402 022626 104003 104400	000010 011664 000340 175452 011656 000340 000200 177776     	001226 001216 177776  175450 175444 175430     	<pre> †ST10: MOV      #10,TSTNO         MOV      #TST11,NEXT         MOV      #340,PS          ;LOCK OUT INTERRUPTS         CLR      @MC.CSR          ;CLEAR CONTROL REGISTER         MOV      #18,@MC.VEC      ;SET UP INTERRUPT SERVICE ADDRESS         MOV      #340,@MC.LVL     ;SET UP INTERRUPT PRIORITY         BIS      #DONE,@MC.CSR    ;SET DONE         CLR      PS               ;ALLOW INTERRUPTS         NOP                      ;DELAY FOR INTERRUPT         BR       2\$               ;NO INTERRUPT, CONTINUE         POP2SP                      ;RESTORE STACK, INTERRUPT         HLT      3                  ;OCCURED, ERROR         SCOPE                      ;CHECK FOR LOOP, ITERATIONS     </pre>
--	--	--	--	--	---

2184  
2185  
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2208  
2209  
2210  
2211  
2212  
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2217  
2218  
2219  
2220  
2221  
2222  
2223  
2224  
2225  
2226  
2227

011664 012737 000011 001226  
011672 012737 011752 001216  
011700 012737 000340 177776  
011706 005077 175364  
011712 012777 011744 175362  
011720 012777 000340 175356  
011726 052777 000100 175342  
011734 005037 177776  
011740 000240  
011742 000402  
011744 022626  
011746 104003  
011750 104400

: TEST 11

-----  
TST11: MOV #11,TSTNO  
MOV #TST12,NEXT  
MOV #340,PS ;LOCK OUT INTERRUPTS  
CLR @MC.CSR ;CLEAR CONTROL REGISTER  
MOV #1\$,@MC.VEC ;SET UP INTERRUPT SERVICE ADDRESS  
MOV #340,@MC.LVL ;SET UP INTERRUPT SERVICE LEVEL  
BIS #INTENA,@MC.CSR ;SET INTERRUPT ENABLE  
CLR PS ;ALLOW INTERRUPTS  
NOP ;DELAY FOR INTERRUPTS  
BR 2\$ ;NO INTERRUPT, CONTINUE  
1\$: POP2SP ;RESTORE STACK  
HLT 3 ;INTERRUPT OCCURED, ERROR  
2\$: SCOPE ;CHECK FOR ITERATIONS, LOOP

\*\*\*\*\* TEST 11 \*\*\*\*\*  
\*VERIFY THAT NO INTERRUPT OCCURS WITH "INTERRUPT ENABLE"  
\*SET AND "DONE" CLEARED.  
\*\*\*\*\*

: TEST 12

-----  
TST12: MOV #12,TSTNO  
MOV #TST13,NEXT  
MOV #340,PS ;LOCK OUT INTERRUPTS  
CLR @MC.CSR ;CLEAR CONTROL REGISTER  
MOV #1\$,@MC.VEC ;SET UP INTERRUPT SERVICE ADDRESS  
MOV #INTENA,@MC.CSR ;SET "INTERRUPT ENABLE"  
MOV #340,@MC.LVL ;SET "INTERRUPT LEVEL"  
CLR PS ;ALLOW INTERRUPTS  
BIS #DONE,@MC.CSR ;SET "DONE"  
NOP ;DELAY FOR INTERRUPT  
HLT 4 ;INTERRUPT OCCURED, ERROR  
BR 2\$ ;CONTINUE  
1\$: POP2SP ;INTERRUPT OCCURED, RESTOR STACK  
2\$: SCOPE ;CHECK FOR ITERATION, LOOP

\*\*\*\*\* TEST 12 \*\*\*\*\*  
\*VERIFY THAT SETTING "DONE" CAUSES AN INTERRUPT  
\*WITH "INTERRUPT ENABLE" SET  
\*\*\*\*\*

2228  
2229  
2230  
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2237  
2238  
2239  
2240  
2241  
2242  
2243  
2244  
2245  
2246  
2247  
2248  
2249  
2250  
2251  
2252  
2253  
2254  
2255  
2256  
2257  
2258  
2259  
2260  
2261  
2262  
2263  
2264  
2265  
2266  
2267  
2268  
2269

\*\*\*\*\* TEST 13 \*\*\*\*\*  
\*VERIFY THAT NO INTERRUPT OCCURS WITH  
\*"INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 7.  
\*\*\*\*\*

: TEST 13

```

TST13:  MOV    #13,TSTNO
        MOV    #TST14,NEXT
        CLR    @MC.CSR          ;CLEAR CONTROL REGISTER
        MOV    #340,PS         ;TO LEVEL 7.
        MOV    #1$,@MC.VEC     ;SET UP INTERRUPT SERVICE ADDRESS
        MOV    #340,@MC.LVL    ;SET UP INTERRUPT SERVICE LEVEL
        MOV    #INTENA,@MC.CSR ;SET INTERRUPT ENABLE
        BIS    #DONE,@MC.CSR   ;GENERATE INTERRUPT
        NOP
        BR     2$              ;DELAY FOR INTERRUPT
        ;NO INTERRUPT, CONTINUE
1$:     POP2SP
        HLT    3                ;RESTORE STACK
        ;INTERRUPT OCCURED, ERROR
2$:     SCOPE                    ;CHECK FOR ITERATION, LOOP

```

\*\*\*\*\* TEST 14 \*\*\*\*\*  
\*VERIFY THAT NO INTERRUPT OCCURS WITH  
\*"INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 6.  
\*\*\*\*\*

: TEST 14

```

TST14:  MOV    #14,TSTNO
        MOV    #TST15,NEXT
        CLR    @MC.CSR          ;CLEAR CONTROL REGISTER
        MOV    #300,PS         ;TO LEVEL 6.
        MOV    #1$,@MC.VEC     ;SET UP INTERRUPT SERVICE ADDRESS
        MOV    #300,@MC.LVL    ;SET UP INTERRUPT SERVICE LEVEL
        MOV    #INTENA,@MC.CSR ;SET INTERRUPT ENABLE
        BIS    #DONE,@MC.CSR   ;GENERATE INTERRUPT
        NOP
        BR     2$              ;DELAY FOR INTERRUPT
        ;NO INTERRUPT, CONTINUE
1$:     POP2SP
        HLT    3                ;RESTORE STACK
        ;INTERRUPT OCCURED, ERROR
2$:     SCOPE                    ;CHECK FOR ITERATION, LOOP

```

M04

2270  
2271 :\*\*\*\*\* TEST 15 \*\*\*\*\*  
2272 :\*VERIFY THAT NO INTERRUPT OCCURS WITH  
2273 :\*"INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 5.  
2274 :\*\*\*\*\*  
2275

: TEST 15

2276 :-----  
2277 :  
2278 012226 012737 000015 001226 TST15: MOV #15,TSTNO  
2279 012234 012737 012316 001216 MOV #TST16,NEXT  
2280 012242 005077 175030 CLR @MC.CSR ;CLEAR CONTROL REGISTER  
2281 012246 012737 000240 177776 MOV #240,PS ;TO LEVEL 5.  
2282 012254 012777 012310 175020 MOV #1\$,@MC.VEC ;SET UP INTERRUPT SERVICE ADDRESS  
2283 012262 012777 000240 175014 MOV #240,@MC.LVL ;SET UP INTERRUPT SERVICE LEVEL  
2284 012270 012777 000100 175000 MOV #INTENA,@MC.CSR ;SET INTERRUPT ENABLE  
2285 012276 052777 000200 174772 BIS #DONE,@MC.CSR ;GENERATE INTERRUPT  
2286 012304 000240 NOP ;DELAY FOR INTERRUPT  
2287 012306 000402 BR 2\$ ;NO INTERRUPT, CONTINUE  
2288 012310 022626 1\$: POP2SP ;RESTORE STACK  
2289 012312 104003 HLT 3 ;INTERRUPT OCCURED, ERROR  
2290 012314 104400 2\$: SCOPE ;CHECK FOR ITERATION, LOOP

2291 :\*\*\*\*\* TEST 16 \*\*\*\*\*  
2292 :\*VERIFY THAT NO INTERRUPT OCCURS WITH  
2293 :\*"INTERRUPT ENABLE" SET AND "DONE" SET AT PRIORITY 4.  
2294 :\*\*\*\*\*  
2295

: TEST 16

2296 :-----  
2297 :  
2298 :  
2299 012316 012737 000016 001226 TST16: MOV #16,TSTNO  
2300 012324 012737 012406 001216 MOV #TST17,NEXT  
2301 012332 005077 174740 CLR @MC.CSR ;CLEAR CONTROL REGISTER  
2302 012336 012737 000200 177776 MOV #200,PS ;TO LEVEL 4.  
2303 012344 012777 012400 174730 MOV #1\$,@MC.VEC ;SET UP INTERRUPT SERVICE ADDRESS  
2304 012352 012777 000200 174724 MOV #200,@MC.LVL ;SET UP INTERRUPT SERVICE LEVEL  
2305 012360 012777 000100 174710 MOV #INTENA,@MC.CSR ;SET INTERRUPT ENABLE  
2306 012366 052777 000200 174702 BIS #DONE,@MC.CSR ;GENERATE INTERRUPT  
2307 012374 000240 NOP ;DELAY FOR INTERRUPT  
2308 012376 000402 BR 2\$ ;NO INTERRUPT, CONTINUE  
2309 012400 022626 1\$: POP2SP ;RESTORE STACK  
2310 012402 104003 HLT 3 ;INTERRUPT OCCURED, ERROR  
2311 012404 104400 2\$: SCOPE ;CHECK FOR ITERATION, LOOP

# N04

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2320 012406 012737 000017 001226
2321 012414 012737 012474 001216
2322 012422 005077 174650
2323 012426 012777 012470 174646
2324 012434 005077 174644
2325 012440 012737 000000 177776
2326 012446 012777 000100 174622
2327 012454 052777 000200 174614
2328 012462 000240
2329 012464 104004
2330 012466 000401
2331 012470 022626
2332 012472 104400
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2341 012474 012737 000020 001226
2342 012502 012737 012562 001216
2343 012510 005077 174562
2344 012514 012777 012556 174560
2345 012522 005077 174556
2346 012526 012737 000040 177776
2347 012534 012777 000100 174534
2348 012542 052777 000200 174526
2349 012550 000240
2350 012552 104004
2351 012554 000401
2352 012556 022626
2353 012560 104400

;***** TEST 17 *****
;*VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT
;*ENABLE" SET AND "DONE" SET AT PRIORITY 0.
;*****

; TEST 17
-----
†ST17: MOV #17,TSTNO
MOV #TST20,NEXT
CLR @MC.CSR ;CLEAR CONTROL REGISTER
MOV #1$,@MC.VEC ;SET UP INTERRUPT SERVICE ADDRESS
CLR @MC.LVL ;SET UP INTERRUPT SERVICE PRIORITY
MOV #0,PS ;SET PROCESSOR PRIORITY TO LEVEL 0.
MOV #INTENA,@MC.CSR ;SET INTERRUPT ENABLE
BIS #DONE,@MC.CSR ;GENERATE INTERRUPT
NOP ;WAIT FOR INTERRUPT
HLT 4 ;NO INTERRUPT, ERROR.
BR 2$ ;CONTINUE
1$: POP2SP ;INTERRUPT OCCURED, RESTORE STACK
2$: SCOPE ;CHECK FOR INTERATIONS, LOOP.

;***** TEST 20 *****
;*VERIFY THAT AN INTERRUPT OCCURS WITH "INTERRUPT
;*ENABLE" SET AND "DONE" SET AT PRIORITY 1.
;*****

; TEST 20
-----
†ST20: MOV #20,TSTNO
MOV #TST21,NEXT
CLR @MC.CSR ;CLEAR CONTROL REGISTER
MOV #1$,@MC.VEC ;SET UP INTERRUPT SERVICE ADDRESS
CLR @MC.LVL ;SET UP INTERRUPT SERVICE PRIORITY
MOV #40,PS ;SET PROCESSOR PRIORITY TO LEVEL 1.
MOV #INTENA,@MC.CSR ;SET INTERRUPT ENABLE
BIS #DONE,@MC.CSR ;GENERATE INTERRUPT
NOP ;WAIT FOR INTERRUPT
HLT 4 ;NO INTERRUPT, ERROR.
BR 2$ ;CONTINUE
1$: POP2SP ;INTERRUPT OCCURED, RESTORE STACK
2$: SCOPE ;CHECK FOR INTERATIONS, LOOP.
```



\*\*\*\*\* TEST 23 \*\*\*\*\*  
\*VERIFY THAT ALL LINE NUMBERS CAN BE WRITTEN INTO AND  
\*READ BACK FROM LINE COUNTER  
\*\*\*\*\*

: TEST 23

000000 012737 000023 001226  
000000 012737 013024 001216  
000000 012737 013000 001220  
000000 012737 013703 007276  
000000 005013 177776  
000000 005037  
000000 005005  
000000 012700 000020  
000000 010513  
000000 011204  
000000 020504  
000000 001401  
000000 104002  
000000 104401  
000000 005205  
000000 005200  
000000 001365  
000000 104400

TST23: MOV #23, TSTNO  
MOV #TST24, NEXT  
MOV #15, LOCK  
MOV MC.CSR, R3  
CLR (R3)  
CLR PS  
CLR R5  
MOV #16, R0  
15: MOV R5, (R3)  
MOV (R3), R4  
CMP R5, R4  
BEQ Z5  
HLT Z  
25: SCOP1  
INC R5  
DEC R0  
BNE Z5  
SCOPE

:SET POINTER  
:CLEAR CONTROL STATUS REGISTER  
:ENABLE INTERRUPTS  
:CLEAR EXPECTED LINE NUMBER  
:SET UP TO TEST 16 LINE NUMBERS  
:SET LINE NUMBER  
:READ BACK LINE NUMBER  
:ARE EXPECTED AND RECEIVED  
:LINE NUMBERS THE SAME  
:LINE NUMBERS DIFFERENT, ERROR  
:CHECK FOR DATA FREEZE  
:UPDATE LINE COUNT  
:UPDATE LINE NUMBER  
:CONTINUE  
:CHECK FOR ITERATION, LOOP

\*\*\*\*\* TEST 24 \*\*\*\*\*  
\*USING "STEP" MODE, VERIFY THAT THE  
\*LINE COUNTER CAN BE STEPPED THRU ALL STATES.  
\*\*\*\*\*

: TEST 24

000000 012737 000024 001226  
000000 012737 013122 001216  
000000 012737 013052 001220  
000000 012703 007276  
000000 005037 177776  
000000 005013  
000000 005005  
000000 012700 000020  
000000 012713 000017  
000000 052713 000400  
000000 104414  
000000 011204  
000000 020504  
000000 001401  
000000 104002  
000000 104401  
000000 005205  
000000 005200  
000000 001365  
000000 104400

TST24: MOV #24, TSTNO  
MOV #TST25, NEXT  
MOV #15, LOCK  
MOV MC.CSR, R3  
15: CLR PS  
CLR (R3)  
CLR R5  
MOV #16, R0  
MOV #17, (R3)  
25: BIS #STEP, (R3)  
DELAY  
MOV (R3), R4  
CMP R5, R4  
BEQ Z5  
HLT Z  
35: SCOP1  
INC R5  
DEC R0  
BNE Z5  
SCOPE

:SET POINTER  
:ENABLE INTERRUPTS  
:CLEAR CONTROL STATUS REGISTER  
:CLEAR EXPECTED LINE NUMBER  
:SET UP TO TEST 16 VALUES  
:FIRST VALUE = 0  
:STEP LINE COUNTER  
:READ LINE COUNTER  
:COMPARE EXPECTED AND  
:RECEIVED LINE NUMBERS  
:LINE COUNTER ERROR  
:CHECK FOR DATA FREEZE  
:UPDATE EXPECTED LINE NUMBER  
:CHECK FOR ITERATIONS, LOOP

\*\*\*\*\* TEST 25 \*\*\*\*\*
\*WRITE 1'S INTO ALL SCANNER MEMORY LOCATIONS.
\*VERIFY THAT ALL LOCATIONS HAVE BEEN WRITTEN
\*TO 1'S.
\*VERIFY THAT "CLEAR SCAN" CLEARS ALL SCANNER
\*MEMORY LOCATIONS.
\*\*\*\*\*

Vertical line of control characters on the left side of the page.

: TEST 25

Assembly code listing for TEST 25, including labels (16, 25, 35, 45, 55, 65, 75), instructions (MOV, CLR, BIS, DEC, BNE, HLT, SCOPI, INC, BEQ, DELAY), addresses (e.g., #25, #16), and comments (e.g., :SET POINTER, :CLEAR CONTROL STATUS REGISTER).



# E05

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013324	012727	000026	001226
013332	012727	013506	001216
013340	012727	013352	001220
013348	012703	007276	
013352	005013		
013354	005037	177776	
013360	012700	000020	
013364	012702	000017	
013370	012713	004000	
013374	032713	000020	
013400	001375		
013402	012713	001000	
013406	050213		
013410	052713	000400	
013414	042713	001000	
013420	012737	000020	001252
013426	012713	000017	
013432	005202		
013434	005001		
013436	052713	000400	
013442	104414		
013444	111304		
013446	010105		
013450	120402		
013452	001002		
013454	052705	070000	
013460	020405		
013462	001402		
013464	104002		
013466	104401		
013470	005201		
013472	005337	001252	
013476	001357		
012500	005300		
012502	001232		
012504	104400		

```

***** TEST 26 *****
*WRITE 1'S INTO SELECTED SCANNER MEMORY LOCATION.
*VERIFY THAT ONLY SELECTED LOCATION WAS WRITTEN INTO.
*****

: TEST 26
-----
1ST26: MOV #26, TSTNO
MOV #TST27, NEXT
MOV #15, LOCK
MOV MC, CSR, R3
15: CLR ; SET POINTER
CLR (R3) ; CLEAR CONTROL STATUS REGISTER
CLR PS ; ENABLE INTERRUPTS
MOV #16, R0 ; SET UP TO TEST 16 ADDRESSES
MOV #17, R2 ; FIRST ADDRESS TO BE TESTED=C
25: MOV #CLASCN, (R3) ; CLEAR SCANNER MEMORY
BIT #BUSY, (R3) ; WAIT FOR CLEAR CYCLE
BNE .-4
MOV #MAINT, (R3) ; SET "MAINTENANCE MODE"
BIS R2, (R3) ; SET LINE COUNTER TO TEST ADDRESS-1
BIS #STEP, (R3) ; WRITE 1'S INTO TEST ADDRESS
BIC #MAINT, (R3) ; CLEAR "MAINTENANCE MODE"
MOV #16, TEMP3 ; SET UP TO TEST ALL 16
MOV #17, (R3) ; SCANNER MEMORY LOCATIONS
INC R2
CLR R1
35: BIS #STEP, (R3) ; ACCESS SCANNER MEMORY
DELAY ; READ CONTENTS OF MEMORY
MOVB (R3), R4 ; SET UP EXPECTED CONTENTS
MOV R1, R5 ; OF SCANNER MEMORY
CMPB R4, R2
BNE 45
BIS #70000, R5
45: CMP R4, R5 ; COMPARE EXPECTED AND RECEIVED
; VALUES
BEQ 55 ; SCANNER MEMORY ERROR
HLT ; CHECK FOR DATA FREEZE
SCJP1
55: INC R1 ; TEST NEXT SCANNED LOCATION
DEC TEMP3 ; UPDATE LINE COUNT
BNE 25
SCOPE ; CHECK FOR ITERATION, LOOP
  
```

F05

\*\*\*\*\* TEST 27 \*\*\*\*\*  
: WITH ALL SCANNER MEMORY LOCATIONS SET TO 1'S.  
: WRITE 0'S INTO SELECTED LOCATION  
: VERIFY THAT ONLY SELECTED LOCATION WAS CLEARED.  
\*\*\*\*\*

TEST 27

013506	012737	000027	001226	ST27:	MOV	#27, TSTNO	
013514	012737	013672	001216		MOV	#TST30, NEXT	
013522	012737	013552	001220		MOV	#25, LOCK	
013530	013703	007276			MOV	MC, CSR, R3	: SET POINTER
013534	005013			1\$:	CLR	(R3)	: CLEAR CONTROL STATUS REGISTER
013536	005037	177776			CLR	PS	: ENABLE INTERRUPTS
013542	012700	000020			MOV	#16, R0	: SET UP TO TEST 16 ADDRESSES
013546	012702	000017			MOV	#17, R2	: FIRST ADDRESS TO BE TESTED=0
013552	012737	000020	001252	2\$:	MOV	#16, TEMP3	: WRITE 1'S INTO ALL SCANNER
013560	012713	001017			MOV	#MAINT+17, (R3)	: MEMORY LOCATIONS
013564	052713	000400		3\$:	BIS	#STEP, (R3)	
013570	005337	001252			DEC	TEMP3	
013574	001373				BNE	3\$	
013576	010213				MOV	R2, (R3)	: SET LINE COUNTER TO TEST ADDRESS-1
013600	052713	000400			BIS	#STEP, (R3)	: WRITE 0'S INTO TEST ADDRESS
013604	012737	000020	001252		MOV	#16, TEMP3	: SET UP TO TEST ALL 16
013612	012713	000017			MOV	#1, (R3)	: SCANNER MEMORY LOCATIONS
013616	005202				INC	R2	
013620	005001				CLR	R1	
013622	052713	000400		4\$:	BIS	#STEP, (R3)	: ACCESS SCANNER MEMORY
013626	104414				DELAY		
013630	111304				MOV8	(R3), R4	: READ CONTENTS OF MEMORY
013632	010105				MOV	R1, R5	: SET UP EXPECTED CONTENTS
013634	120402				CMFB	R4, R2	: OF SCANNER MEMORY
013636	001002				BNE	5\$	
013640	052705	070000			BIS	#70000, R5	
013644	020405			5\$:	CMF	R4, R5	: COMPARE EXPECTED AND
013646	001402				BEQ	6\$	: RECEIVED VALUES
013650	104002				HLT	2	: SCANNER MEMORY ERROR
013652	104401				SCOP1		: CHECK FOR DATA FREEZE
013654	005201			6\$:	INC	R1	
013656	005337	001252			DEC	TEMP3	: TEST NEXT SCANNER LOCATION
013660	001357				BNE	4\$	
013664	005300				DEC	R0	: UPDATE ADDRESS COUNT
013668	001331				BNE	2\$	
013672	104400				SCOP2		: CHECK FOR ITERATION, LOOP

# G05

```

***** TEST 30 *****
*VERIFY THAT "CLEAR MULTIPLXER" CLEARS ALL MULTIPLEXER
*FUNCTION FLIP-FLOPS
*****
  
```

000000  
 000001  
 000002  
 000003  
 000004  
 000005  
 000006  
 000007  
 000008  
 000009  
 000010  
 000011  
 000012  
 000013  
 000014  
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 000023  
 000024  
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 000029  
 000030

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: TEST 30
-----
TST30: MOV #30,TSTNO
MOV #TST31,NEXT
MOV #35,LOCK
MOV MC.CSR,R3 ;SET POINTER
1$: CLR (R3) ;CLEAR CONTROL REGISTER
CLR PS ;ENABLE INTERRUPTS
MOV #16,R0 ;SET UP TO TEST 16 LINES
2$: MOV #17,QMC.LSR ;WRITE 15 INTO ALL MULTIPLEXER
BIS #STEP,(R3) ;FUNCTION FLIPFLOPS
DEC R0
BNE 2$
CLR TEMP3 ;SET UP FOR 16 LINES
MOV #16,R0
3$: MOV #CLRMUX,(R3) ;CLEAR MULTIPLEXER
4$: MOV TEMP3,(R3) ;SELECT LINE
MOV QMC.LSR,R4 ;READ LINE STATUS REGISTER
CLR R5 ;EXPECT 05
TST R4 ;WAS LINE STATUS REGISTER CLEARED
BEQ 5$
HLT 2 ;LINE STATUS ERROR
SCOPI ;CHECK FOR LOOP ON SAME DATA
5$: INC R5 ;EXPECT LINE ENABLE
BIS #LINENA,QMC.LSR ;SET LINE ENABLE ON SELECTED LINE
MOV QMC.LSR,R4 ;READ LINE STATUS REGISTER
CMP R5,R4 ;IS ANYTHING BUT LINE ENABLE SET
BEQ 6$
HLT 2 ;LINE STATUS ERROR
SCOPI ;CHECK FOR LOOP ON SAME DATA
6$: INC TEMP3 ;UPDATE LINE NUMBER
CLR QMC.LSR ;CLEAR CURRENT LINE
DEC R0 ;CONTINUE IF ALL LINES NOT
BNE 4$ ;TESTED
SCOPE ;CHECK FOR ITERATIONS. LOOP
  
```

# H05

\*\*\*\*\* TEST 31 \*\*\*\*\*  
:WRITE 1'S INTO ALL SCANNER MEMORY LOCATIONS  
:SET "LINE ENABLE FOR ALL LINES"  
:VERIFY THAT AN INTERRUPT OCCURS FOR EACH LINE  
:\*\*\*\*\*

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2639  
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2641  
2642  
2643  
2644  
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2646  
2647  
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2649  
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2680  
2681  
2682

014050 012737 000031 001226  
014056 012737 014302 001216  
014054 012737 014076 001220  
014072 013703 007276  
014076 012713 002000  
014102 005013  
014104 005037 177776  
014110 012700 000020  
014114 012713 001017  
014120 052713 000400  
014124 012777 000001 173146  
014132 005300  
014134 001371  
014136 012705 070340  
014142 012777 014252 173132  
014150 013777 177776 173126  
014156 012700 000020  
014152 012713 000117  
014166 012737 000340 177776  
014174 052713 000040  
014200 005037 177776  
014204 005037 001270  
014210 105713  
014212 100410  
014214 104414  
014216 000240  
014220 000240  
014222 062737 000001 001270  
014230 001367  
014232 104006  
014234 012737 000340 177776  
014242 011304  
014244 104004  
014246 104401  
014250 000406  
014252 022626  
014254 011304  
014256 020504  
014260 001402  
014262 104002  
014264 104401  
014266 042713 000240  
014272 005205  
014274 005300  
014276 001333  
014300 104400

: TEST 31  
-----  
1\$:  
2\$:  
3\$:  
4\$:  
5\$:  
SCOPE

1\$T31: MOV #31, TSTNO  
MOV #T32, NEXT  
MOV #1\$, LOCK  
MOV MC.CSR, R3  
1\$: MOV #CLRMUX, (R3)  
CLR (R3)  
CLR PS  
MOV #16, R0  
MOV #MAINT+17, (R3)  
2\$: BIS #STEP, (R3)  
MOV #LINEA, &MC.LSR  
DEC R0  
BNE 2\$  
MOV #70340, R5  
MOV #4\$, &MC.VEC  
MOV PS, &MC.LVL  
MOV #16, R0  
MOV #INTENA+17, (R3)  
3\$: MOV #340, PS  
BIS #SCNENA, (R3)  
CLR PS  
CLR SAVR4  
TSTB (R3)  
BMI .+22  
NOP  
NOP  
ADD #1, SAVR4  
BNE .-20  
HLT 6  
MOV #340, PS  
MOV (R3), R4  
HLT 4  
SCOPE1  
BR 5\$  
4\$: POP2SP  
MOV (R3), R4  
CMP R5, R4  
BEQ 5\$  
HLT 2  
5\$: SCOPE1  
BIC #SCNENA+DONE, (R3)  
INC R5  
DEC R0  
BNE 3\$  
SCOPE

:SET POINTER  
:CLEAR ALL MULTIPLEXER FLIPFLOPS  
:CLEAR CONTROL REGISTER  
:ENABLE INTERRUPTS  
:SET UP TO WRITE 1'S INTO  
:ALL SCANNER MEMORY LOCATION  
:WRITE A LOCATION  
:LET "LINE ENABLE"  
:EXPECT "DONE"+"SCNENA"+"CCF"+"CSF"+"SECRXF"  
:SET UP LOCAL INTERRUPT SERVICE  
:SERVICE AT LEVEL 7  
:SET INTERRUPT ENABLE  
:LOCK OUT INTERRUPTS  
:START SCANNER  
:ENABLE INTERRUPTS  
:WAIT FOR DONE  
:INTERRUPT DID NOT OCCUR  
:ERROR  
:CONTROL STATUS ERROR  
:CHECK FOR LOOP ON SAME DATA  
:INTERRUPT OCCURED, REPOSITION STACK  
:READ CONTROL STATUS  
:ARE EXPECTED AND RECEIVED  
:REGISTERS THE SAME  
:NO. LINE STATUS ERROR  
:CHECK FOR LOOP WITH CURRENT DATA  
:CLEAR SCAN ENABLE AND DONE  
:UPDATE EXPECTED RESULT  
:CONTINUE IF NOT DONE  
:CHECK FOR ITERATIONS, LOOP

\*\*\*\*\* TEST 32 \*\*\*\*\*  
:WRITE 1'S INTO ALL MULTIPLEXER FUNCTION FLIP-FLOPS  
:CLEAR SCANNER MEMORY  
:VERIFY THAT AN INTERRUPT OCCURS FOR EACH LINE  
:THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.  
\*\*\*\*\*

: TEST 32

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2728  
2729  
2730  
2731  
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2734  
2735  
2736  
2737  
2738  
014302 012737 000032 001226  
014310 012737 014646 001216  
014316 012737 014434 001220  
014324 005000  
014326 005737 001416  
014332 100402  
014334 062700 000004  
014340 005737 001420  
014344 100402  
014346 062700 000004  
014352 005737 001422  
014356 100402  
014360 062700 000004  
014364 005737 001424  
014370 100402  
014372 062700 000004  
014376 005700  
014400 001001  
014402 000000  
014404 010037 007274  
014410 005737 007256  
014414 001405  
014416 013737 001216 001214  
014424 000177 164564  
014430 013703 007276  
014434 012700 000020  
014440 012713 002000  
014444 005013  
014446 005037 177776  
014452 012777 000017 172620  
014460 052713 000400  
014464 005300  
014466 001371  
014470 012713 004000  
014474 032713 000020  
014500 001375  
014502 013700 007274  
014506 012705 170340  
014512 012777 014616 172562  
014520 013777 177776 172556  
014526 012713 000117  
014532 012737 000340 177776  
014540 052713 000040  
014544 005037 177776  
014550 005037 001270  
014554 105713  
014556 100410

TST32: MOV #32, TSTNO  
MOV #TST33, NEXT  
MOV #15, LOCK  
CLR RO  
TST L00.03  
BMI 68\$  
ADD #4, RO  
TST L04.07  
BMI 69\$  
ADD #4, RO  
TST L08.11  
BMI 70\$  
ADD #4, RO  
TST L12.15  
BMI 71\$  
ADD #4, RO  
TST RO  
BNE .+4  
HALT  
MOV RO, TOTAL  
TST TURFLG  
BEQ 65\$  
MOV NEXT, RETURN  
JMP @RETURN  
65\$: MOV MC, CSR, R3  
1\$: MOV #16, RO  
MOV #CLRMUX, (R3)  
CLR (R3)  
CLR PS  
2\$: MOV #17, @MC, LSR  
BIS #STEP, (R3)  
DEC RO  
BNE 2\$  
MOV #CLRSCN, (R3) :CLEAR SCANNER MEMORY  
BIT #BUSY, (R3) :WAIT FOR CLEAR CYCLE TO COMPLETE  
BNE -4  
MOV TOTAL, RO  
MOV #170340, R5 :FIRST EXPECTED RESULT  
MOV #4\$, @MC, VEC :SET UP LOCAL INTERRUPT RETURN  
MOV PS, @MC, LVL  
MOV #INTENA+17, (R3) :SET INTERRUPT ENABLE  
MOV #340, PS :LOCK OUT INTERRUPTS  
BIS #SCNENA, (R3) :START SCANNER  
CLR PS :ENABLE INTERRUPTS  
CLR SAVR4  
TSTB (R3) :WAIT FOR DONE  
BMI .+22

:TEST CAN NOT RUN WITH NO LINE CARDS!!

:SET POINTER  
:WRITE 1S INTO ALL  
:CLEAR MULTIPLEXER  
:MULTIPLEXER FUNCTION  
:ENABLE TELETYPE INTERRUPTS  
:FLIPFLOPS  
:CLEAR SCANNER MEMORY  
:WAIT FOR CLEAR CYCLE TO COMPLETE  
:FIRST EXPECTED RESULT  
:SET UP LOCAL INTERRUPT RETURN  
:SET INTERRUPT ENABLE  
:LOCK OUT INTERRUPTS  
:START SCANNER  
:ENABLE INTERRUPTS  
:WAIT FOR DONE

# J05

2739	014560	104414		DELAY	
2740	014562	000240		NOP	
2741	014564	000240		NOP	
2742	014566	062737	000001 001270	ADD	#1,SAVR4
2743	014574	001367		BNE	-20
2744	014576	104006		HLT	6
2745	014600	012737	000340 177775	MOV	#340,FS
2746	014606	011304		MOV	(R3),R4
2747	014610	104004		HLT	4
2748	014612	104401		SCOPI	
2749	014614	000406		BR	5\$
2750	014616	022626	4\$:	POP2SP	
2751	014620	011304		MOV	(R3),R4
2752	014622	020504		CMP	R5,R4
2753	014624	001402		BEQ	5\$
2754	014626	104002		HLT	2
2755	014630	104401		SCOPI	
2756	014632	042713	000240 5\$:	BIC	#SCNENA+DONE,(R3)
2757	014636	005205		INC	R5
2758	014640	005300		DEC	R0
2759	014642	001333		BNE	3\$
2760	014644	104400		SCOPE	

```

:LOCK OUT INTERRUPTS
:READ CONTROL STATUS
:INTERRUPT DID NOT OCCUR
:CHECK FOR LOOP ON CURRENT DATA
:CONTINUE
:INTERRUPT OCCURED, RESTORE STACK
:READ CONTROL STATUS REGISTER
:COMPARE TO EXPECTED RESULT

:CONTROL STATUS ERROR
:CHECK FOR LOOP ON CURRENT DATA
:CLEAR SCAN ENABLE AND DONE
:UPDATE EXPECTED RESULT
:CONTINUE IF ALL
:LINE NOT TESTED
:CHECK FOR ITERATIONS, LOOP
  
```

# K05

```

2761 :*****TEST 33*****
2762 :*VERIFY THAT LINE ENABLE FUNCTION FLIP-FLOP CAN
2763 :*BE SET AND CLEARED FOR SELECTED LINE
2764 :*THIS TEST IS DONE IF THE H325 TURN AROUND IS USEFUL
2765 :*MODEM CONTROL LINES *MUST* BE CONTIGUOUS FROM LINE 00.
2766 :*****
2767
2768 : TEST 33
2769 -----
2770 014646 012737 000033 001226 TST33: MOV #33,TSTNO
2771 014654 012737 015046 001216 MOV #TST34,NEXT
2772 014652 005737 007256 TST TURFLG ;TURN AROUND H861 OR H325?
2773 014666 001005 SNE 1$ ;BR IF H325
2774 014670 013737 001216 001214 MOV NEXT,RETURN
2775 014676 000177 164312 JMP @RETURN
2776 014702 005077 172370 1$: CLR @MC.CSR ;CLEAR CONTROL STATUS REGISTER
2777 014706 005037 177776 CLR PS ;ZERO PSW.
2778 014712 013701 007260 MOV LINE,R1 ;SET LINE IMAGE
2779 014716 012777 002000 172352 2$: MOV #CLRMUX,@MC.CSR ;CLEAR MUX
2780 014724 012702 000020 MOV #16,R2 ;SET FOR 16 LINES
2781 014730 010177 172342 MOV R1,@MC.CSR ;SELECT LINE TO BE TESTED
2782 014734 012777 000001 172336 MOV #LINENA,@MC.LSR ;SET LINE ENABLE FUNCTION FLIP-FLOP
2783 014742 005077 172330 CLR @MC.CSR ;ZERO CSR
2784 014746 005005 3$: CLR R5 ;SET EXPECTED
2785 014750 017704 172324 MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER
2786 014754 117703 172316 MOVB @MC.CSR,R3 ;READ CONTROL STATUS REGISTER
2787 014760 042703 177760 BIC #1<17>,R3 ;CLEAR UNWANTED BITS
2788 014764 020103 CMP R1,R3 ;IF LINE NUMBER=SELECTED LINE NUMBER,
2789 014766 001002 BNE 4$ ;EXCEPT LINE ENABLE FUNCTION FLIP FLOP
2790 014770 012705 000001 MOV #LINENA,R5 ;SET "GOOD"
2791 ;TO BE SET
2792 014774 020504 4$: CMP R5,R4 ;COMPARE EXPECTED AND RECEIVED
2793 014776 001401 BEQ 5$ ;RESULTS
2794 015000 104001 HLT 1 ;R5=EXPECTED R4=FOUND
2795 015002 052777 000400 172266 5$: BIS #STEP,@MC.CSR ;EXAMINE NEXT LINE
2796 015010 005302 DEC R2 ;ALL LINES DONE?
2797 015012 001355 BNE 3$ ;BR IF NO
2798 015014 005005 CLR R5 ;CLEAR "GOOD"
2799 015016 010177 172254 6$: MOV R1,@MC.CSR ;LOAD LINE
2800 015022 010103 MOV R1,R3 ;SET LINE COUNTER TO SELECTED LINE
2801 015024 005077 172250 CLR @MC.LSR ;CLEAR LINE ENABLE FLIP FLOP
2802 015030 104414 DELAY ;DELAY FOR CABLE
2803 015032 017704 172242 MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER
2804 015036 005701 TST R4 ;WAS LINE ENABLE FUNCTION FLIP FLOP
2805 015040 001401 BEQ .+4 ;CLEARED
2806 015042 104001 HLT 1 ;R5=EXPECTED R4=FOUND
2807 015044 104400 7$: SCOPE ;CHECK FOR ITERATIONS. LOOP
  
```

```

2808 :***** TEST 34 *****
2809 :*VERIFY THAT TERMINAL READY FUNCTION FLIP-FLOP CAN
2810 :*BE SET AND CLEARED FOR SELECTED LINE
2811 :*THIS TEST IS DONE IF THE H325 TURN AROUND IS USED
2812 : MODERN CONTROL LINES *MUST* BE CONTIGUOUS FROM LINE 00.
2813 :*****
2814
2815 : TEST 34
2816 -----
2817 015046 012737 000034 001226 1ST34: MOV #34,TSTNO
2818 015054 012737 015245 001216 MOV #TST35,NEXT
2819 015052 005737 007256 TST TURFLG ;TURN AROUND H861 OR H325?
2820 015066 001005 SNE 1$ ;BR IF H325
2821 015070 013737 001216 001214 MOV NEXT,RETURN
2822 015076 000177 164112 JMP @RETURN
2823 015102 005077 172170 1$: CLR @MC.CSR ;CLEAR CONTROL STATUS REGISTER
2824 015106 005037 177776 CLR PS ;ZERO PSW.
2825 015112 013701 007260 MOV LINE,R1 ;SET LINE IMAGE
2826 015116 012777 002000 172152 2$: MOV #CLRMUX,@MC.CSR ;CLEAR MUX
2827 015124 012702 000020 MOV #16,R2 ;SET FOR 16 LINES
2828 015130 010177 172142 MOV R1,@MC.CSR ;SELECT LINE TO BE TESTED
2829 015134 012777 000002 172136 MOV #TRMRDY,@MC.LSR ;SET TERMINAL READY FUNCTION FLIP-FLOP
2830 015142 005077 172130 CLR @MC.CSR ;ZERO CSR
2831 015146 005005 3$: CLR R5 ;SET EXPECTED
2832 015150 017704 172124 MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER
2833 015154 117703 172116 MOVB @MC.CSR,R3 ;READ CONTROL STATUS REGISTER
2834 015160 042703 177760 BIC #C<17>,R3 ;CLEAR UNWANTED BITS
2835 015164 020103 CMP R1,R3 ;IF LINE NUMBER=SELECTED LINE NUMBER,
2836 015166 001002 BNE 4$ ;EXCEPT TERMINAL READY FUNCTION FLIP FLOP
2837 015170 012705 000002 MOV #TRMRDY,R5 ;SET "GOOD"
2838 ;TO BE SET
2839 015174 023504 4$: CMP R5,R4 ;COMPARE EXPECTED AND RECEIVED
2840 015176 001401 BEQ 5$ ;RESULTS
2841 015200 104001 HLT 1 ;R5=EXPECTED R4=FOUND
2842 015202 052777 000400 172066 5$: BIS #STEP,@MC.CSR ;EXAMINE NEXT LINE
2843 015210 005302 DEC R2 ;ALL LINES DONE?
2844 015212 001355 BNE 3$ ;BR IF NO
2845 015214 005005 CLR R5 ;CLEAR "GOOD"
2846 015216 010177 172054 6$: MOV R1,@MC.CSR ;LOAD LINE
2847 015222 010103 MOV R1,R3 ;SET LINE COUNTER TO SELECTED LINE
2848 015224 005077 172050 CLR @MC.LSR ;CLEAR TERMINAL READY FLIP FLOP
2849 015230 104414 DELAY ;DELAY FOR CABLE
2850 015232 017704 172042 MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER
2851 015236 005704 TST R4 ;WAS TERMINAL READY FUNCTION FLIP FLOP
2852 015240 001401 BEQ .+4 ;CLEARED
2853 015242 104001 HLT 1 ;R5=EXPECTED R4=FOUND
2854 015244 104400 7$: SCOPE ;CHECK FOR ITERATIONS. LOOP
    
```







\*\*\*\*\* TEST 37 \*\*\*\*\*  
: \*VERIFY THAT RING IS SET IF "LINE ENABLE"  
: \*AND TERMINAL ARE SET FOR SELECTED LINE.  
: \*THIS TEST IS DONE IF THE H325 TURN AROUND IS USED  
: \*MODEM CONTROL LINES \*MUST\* BE CONTIGUOUS FROM LINE 00.  
: \*\*\*\*\*

: TEST 37

015746 012737 000037 001226  
015748 012737 016044 001216  
015750 012737 007256  
015752 001005  
015754 013737 001216 001214  
015756 000177 163312  
015758 005077 171370  
015760 005037 177776  
015762 013701 007260  
015764 012702 000020  
015766 010177 171350  
015768 012777 000003 171344  
015770 005077 171336  
015772 005005  
015774 017704 171332  
015776 117703 171324  
015778 042703 177760  
015780 020103  
015782 001002  
015784 012705 000203  
015786 020405  
015788 001401  
015790 104001  
015792 052777 000400 171274  
015794 005302  
015796 001355  
015798 012705 000001  
015800 010103  
015802 010177 171256  
015804 042777 000002 171252  
015806 104414  
015808 017704 171244  
015810 020504  
015812 001401  
015814 104201  
015816 104400

```

:-----
: TEST 37
:
: ST37:  MOV  #37, TSTNO
:         MOV  #TST40, NEXT
:         TST  TURFLG
:         BNE  IS
:         MOV  NEXT, RETURN
:         JMP  @RETURN
: 15:    CLR  @MC.CSR
:         CLR  PS
:         MOV  LINE, R1
: 25:    MOV  #16, R2
:         MOV  R1, @MC.CSR
:         MOV  @LINENA+TRMADY, @MC.LSR
:         CLR  @MC.CSR
: 35:    CLR  R5
:         MOV  @MC.LSR, R4
:         MOV  @MC.CSR, R3
:         BIC  #C(17), R3
:         CMP  R1, R3
:         BNE  #48
:         MOV  @LINENA+TRMADY+RING, R5
: 45:    CMP  R4, R5
:         BEQ  #58
:         HLT  1
: 55:    BIS  #STEP, @MC.CSR
:         DEC  R2
:         BNE  #38
:         MOV  @LINENA, R5
: 65:    MOV  R1, R3
:         MOV  R1, @MC.CSR
:         BIC  #TRMADY, @MC.LSR
:         DELAY
:         MOV  @MC.LSR, R4
:         CMP  R5, R4
:         BEQ  #44
:         HLT  1
: 75:    SCOPE
:
: :TURN AROUND H861 OR H325?
: BR IF H325
:
: :CLEAR CONTROL REGISTER
: :ZERO PSW
: :LINE NUMBER
: :16 LINES
: :SELECT A LINE
: :SET LINE ENABLE +TRMADY
: :CLEAR CONTROL REGISTER
: :CLEAR EXPECTED RESULT
: :READ LINE STATUS
: :READ LINE NUMBER
: :CLEAR UNWANTED BITS
: :IF RECEIVED LINE=SELECTED LINE
: :EXPECT LINE ENABLE AND
:
: :RING IS SET
: :COMPARE EXPECTED AND
: :RECEIVED RESULTS
: :R5=EXPECTED R4=FOUND
: :UPDATE LINE COUNTER
: :CONTINUE IF ALL CHECKS
: :ARE NOT DONE FOR THIS LINE
: :EXPECT LINE ENABLE
: :ON SELECTED LINE
: :SELECT LINE
: :CLEAR TERMINAL
: :DELAY FOR CABLE
: :READ LINE STATUS REGISTER
: :ONLY LINE ENABLE SHOULD BE
: :SET ON THIS LINE
: :R5=EXPECTED R4=FOUND
: :CHECK FOR ITERATIONS, LOOP

```

\*\*\*\*\* TEST 40 \*\*\*\*\*  
: \*VERIFY THAT CLEAR TO SEND AND CARRIER ARE SET IF "LINE ENABLE"  
: \*AND REQUEST TO SEND ARE SET FOR SELECTED LINE.  
: \*THIS TEST IS DONE IF THE M325 TURN AROUND IS USED  
: \*MODEM CONTROL LINES \*MUST\* BE CONTIGUOUS FROM LINE 00.  
\*\*\*\*\*

: TEST 40

016044	012737	000040	001226	TST40:	MOV	#40, TSTNO	
016055	012737	016242	001215		MOV	#TST41, NEXT	
016066	005737	007256			TST	TURFLG	:TURN AROUND M861 OR M325?
016064	001005				BNE	15	:BR IF M325
016066	013737	001216	001214		MOV	NEXT, RETURN	
016074	000177	163114			JMP	3RETURN	
016100	005077	171172		15:	CLR	QMC.CSR	:CLEAR CONTROL REGISTER
016104	005037	177776			CLR	PS	:ZERO PSW
016110	013701	007260			MOV	LINE, R1	:LINE NUMBER
016114	012702	000020		25:	MOV	#16, R2	:16 LINES
016120	010177	171152			MOV	R1, QMC.CSR	:SELECT A LINE
016124	012777	000005	171146		MOV	#LINENA+RS, QMC.LSR	:SET LINE ENABLE +RS
016132	005077	171140			CLR	QMC.CSR	:CLEAR CONTROL REGISTER
016136	005005			35:	CLR	R5	:CLEAR EXPECTED RESULT
016140	017704	171134			MOV	QMC.LSR, R4	:READ LINE STATUS
016144	117703	171126			MOVB	QMC.CSR, R3	:READ LINE NUMBER
016150	042703	177760			BIC	#C<17>, R3	:CLEAR UNWANTED BITS
016154	020102				CMP	R1, R3	:IF RECEIVED LINE=SELECTED LINE
016156	001002				BNE	45	:EXPECT LINE ENABLE AND
016160	012705	000145			MOV	#LINENA+RS+00+05, R5	
016164	020405			45:	CMP	R4, R5	:CLEAR TO SEND AND CARRIER ARE SET
016166	001401				BEQ	55	:COMPARE EXPECTED AND
016170	104001				HLT	!	:RECEIVED RESULTS
016172	052777	000400	171076	55:	BIS	#STEP, QMC.CSR	:R5=EXPECTED R4=FOUND
016200	005302				DEC	R2	:UPDATE LINE COUNTER
016202	001355				BNE	35	:CONTINUE IF ALL CHECKS
016204	012705	000001			MOV	#LINENA, R5	:ARE NOT DONE FOR THIS LINE
016210	010102			65:	MOV	R1, R3	:EXPECT LINE ENABLE
016212	010177	171060			MOV	R1, QMC.CSR	:ON SELECTED LINE
016216	042777	000004	171054		BIC	#R5, QMC.LSR	:SELECT LINE
016224	104414				DELAY		:CLEAR REQUEST TO SEND
016226	017704	171046			MOV	QMC.LSR, R4	:DELAY FOR CABLE
016232	020504				CMP	R5, R4	:READ LINE STATUS REGISTER
016234	001401				BEQ	+4	:ONLY LINE ENABLE SHOULD BE
016236	104001				HLT	!	:SET ON THIS LINE
016240	104400			75:	SCOPE		:R5=EXPECTED R4=FOUND
							:CHECK FOR ITERATIONS. LOOP

\*\*\*\*\* TEST 41 \*\*\*\*\*  
\*VERIFY THAT DATA SET READY (SECRX IF ASYNC LC) IS SET IF "LINE ENABLE"  
\*AND NEW SYNC (SECTX IF ASYNC LC) ARE SET FOR SELECTED LINE.  
\*THIS TEST IS DONE IF THE H325 TURN AROUND IS USED  
\*MODEM CONTROL LINES \*MUST\* BE CONTIGUOUS FROM LINE 00.  
\*\*\*\*\*

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000001  
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000100

```

: TEST 41
-----
TST41:  MOV    #4, TSTNO
        MOV    #TST42, NEXT
        TST    TURFLG
        BNE    IS
        MOV    NEXT, RETURN
        JMP    @RETURN
15:     CLR    @MC.CSR
        CLR    PS
        MOV    LINE, R1
25:     MOV    #16, R2
        MOV    R1, @MC.CSR
        MOV    @LINEA+NS, @MC.LSR
        CLR    @MC.CSR
35:     CLR    R5
        MOV    @MC.LSR, R4
        MOVB   @MC.CSR, R3
        BIC    #1<17>, R3
        CMP    R1, R3
        BNE    45
        MOV    #LINEA+NS+DSR, R5
45:     CMP    R4, R5
        BEQ    55
        HLT    1
55:     BIS    #STEP, @MC.CSR
        DEC    R2
        BNE    35
        MOV    #LINEA, R5
65:     MOV    R1, R3
        MOV    R1, @MC.CSR
        BIC    #NS, @MC.LSR
        DELAY
        MOV    @MC.LSR, R4
        CMP    R5, R4
        BEQ    .+4
        HLT    1
75:     SCOPE

```

:TURN AROUND H861 OR H325?  
:SR IF H325  
:CLEAR CONTROL REGISTER  
:ZERO PSW  
:LINE NUMBER  
:16 LINES  
:SELECT A LINE  
:SET LINE ENABLE +NS  
:CLEAR CONTROL REGISTER  
:CLEAR EXPECTED RESULT  
:READ LINE STATUS  
:READ LINE NUMBER  
:CLEAR UNWANTED BITS  
:IF RECEIVED LINE=SELECTED LINE  
:EXPECT LINE ENABLE AND  
:DATA SET READY (SECRX IF ASYNC LC) IS SET  
:COMPARE EXPECTED AND  
:RECEIVED RESULTS  
:R5=EXPECTED R4=FOUND  
:UPDATE LINE COUNTER  
:CONTINUE IF ALL CHECKS  
:ARE NOT DONE FOR THIS LINE  
:EXPECT LINE ENABLE  
:ON SELECTED LINE  
:SELECT LINE  
:CLEAR NEW SYNC (SECTX IF ASYNC LC)  
:DELAY FOR CABLE  
:READ LINE STATUS REGISTER  
:ONLY LINE ENABLE SHOULD BE  
:SET ON THIS LINE  
:R5=EXPECTED R4=FOUND  
:CHECK FOR ITERATIONS, LOOP

\*\*\*\*\* TEST 42 \*\*\*\*\*  
: \*VERIFY THAT LINE ENABLE FUNCTION FLIP-FLOP CAN  
: \*BE SET AND CLEARED FOR SELECTED LINE  
: \*THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.  
: \*MODEM CONTROL LINES \*MUST\* BE CONTIGUOUS FROM LINE 00.  
: \*\*\*\*\*

TEST 42

Address	Hex	Hex	Hex	Hex	Instruction	Comment
016440	012737	000C42	001226	1ST42:	MOV #42, TSTNO	
016446	012737	016664	001216		MOV #TST43, NEXT	
016454	205737	007256			TST TURFLG	:TURN AROUND H861 OP H325?
016460	001405				BEQ 1\$	:BR IF H861
016462	013737	001216	001214		MOV NEXT, RETURN	
016470	000177	162520			JMP @RETURN	
016474	005077	170576		1\$:	CLR @MC.CSR	:CLEAR CONTROL STATUS REGISTER
016500	005037	177776			CLR PS	:ZERO PSW.
016504	013700	007274			MOV TOTAL, R0	:SET THE TOTAL NUMBER OF LINES TO BE TESTED IN R
016510	005001				CLR R1	
016512	012737	016520	001220		MOV #2\$, LOCK	
016520	012777	002000	170550	2\$:	MOV @CLRMUX, @MC.CSR	:CLEAR MUX
016526	012702	000020			MOV #16, R2	:SET FOR 16 LINES
016532	010177	170540			MOV R1, @MC.CSR	:SELECT LINE TO BE TESTED
016536	010137	007260			MOV R1, LINE	:SET IMAGE
016542	012777	000001	170530		MOV @LINEA, @MC.LSR	:SET LINE ENABLE FUNCTION FLIP-FLOP
016550	005077	170522			CLR @MC.CSR	:ZERO CSR
016554	005005			3\$:	CLR R5	:SET EXPECTED
016556	017704	170516			MOV @MC.LSR, R4	:READ LINE STATUS REGISTER
016562	117703	170510			MOV @MC.CSR, R3	:READ CONTROL STATUS REGISTER
016566	042703	177760			BIC #1<17>, R3	:CLEAR UNWANTED BITS
016572	020103				CMP R1, R3	:IF LINE NUMBER=SELECTED LINE NUMBER,
016574	001002				BNE 4\$	:EXCEPT LINE ENABLE FUNCTION FLIP FLOP
016576	012705	000001			MOV @LINEA, R5	:SET "GOOD"
016602	020504			4\$:	CMP R5, R4	:TO BE SET
016604	001401				BEQ 5\$	:COMPARE EXPECTED AND RECEIVED
016606	104001				HLT 1	:RESULTS
016610	052777	000400	170450	5\$:	BUS #STEP, @MC.CSR	:R5=EXPECTED R4=FOUND
016616	005302				DEC R2	:EXAMINE NEXT LINE
016620	001355				BNE 3\$	:ALL LINES DONE
016622	005005				CLR R5	:BR IF NO
016624	010177	170446		6\$:	MOV R1, @MC.CSR	:CLEAR "GOOD"
016630	010103				MOV R1, R3	:LOAD LINE
016632	005077	170442			CLR @MC.LSR	:SET LINE COUNTER TO SELECTED LINE
016636	104414				DELAY	:CLEAR LINE ENABLE FLIP FLOP
016640	017704	170434			MOV @MC.LSR, R4	:DELAY FOR CABLE
016644	005704				TST R4	:READ LINE STATUS REGISTER
016646	001401				BEQ +4	:WAS LINE ENABLE FUNCTION FLIP FLOP
016650	104001				HLT 1	:CLEAREC
016652	104401				SCOPI	:R5=EXPECTED R4=FOUND
016654	005201				INC R1	
016656	005303				DEC R0	
016658	001317				BNE 2\$	
016660	104400			7\$:	SCOPE	:CHECK FOR ITERATIONS, LOOP

```

:***** TEST 43 *****
:*VERIFY THAT TERMINAL READY FUNCTION FLIP-FLOP CAN
:*BE SET AND CLEARED FOR SELECTED LINE
:*THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.
:* MODEM CONTROL LINES *MUST* BE CONTIGUOUS FROM LINE 00.
:*****
  
```

: TEST 43

017026 017030 017032 017034 017042 017044 017046 017050 017054 017056 017062 017064 017070 017072 017074 017076 017100 017102 017104 017106	020504 001401 104001 052777 005302 001355 005305 010177 010103 005077 104414 017704 005704 001401 104001 104401 005201 005300 001317 104400	000400 170234 000400 170222 170216 170210	001226 001216 007256 001214 001214 162274 170352 177776 007274 001220 170324 000020 170314 007260 170304 170272 170264 177760 000002 000002	15: 25: 35: 45: 55: 65: 75:	<pre> TST43: MOV #43,TSTNO         MOV #TST44,NEXT         TST TURFLG ;TURN AROUND H861 OR H325?         SEQ 15 ;BR IF H861         MOV NEXT,RETURN         JMP @RETURN 15: CLR @MC.CSR ;CLEAR CONTROL STATUS REGISTER      CLR PS ;ZERO PSW.      MOV TOTAL,R0 ;SET THE TOTAL NUMBER OF LINES TO BE TESTED IN R      CLR R1      MOV #25,LOCK 25: MOV #CLRMUX,@MC.CSR ;CLEAR MUX      MOV #16,R2 ;SET FOR 16 LINES      MOV R1,@MC.CSR ;SELECT LINE TO BE TESTED      MOV R1,LINE ;SET IMAGE      MOV #TRMRDY,@MC.LSR ;SET TERMINAL READY FUNCTION FLIP-FLOP      CLR @MC.CSR ;ZERO CSR      CLR R5 ;SET EXPECTED      MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER      MOVB @MC.CSR,R3 ;READ CONTROL STATUS REGISTER      BIC #17,R3 ;CLEAR UNWANTED BITS      CMP R1,R3 ;IF LINE NUMBER=SELECTED LINE NUMBER.      BNE 45 ;EXCEPT TERMINAL READY FUNCTION FLIP FLOP      MOV #TRMRDY,R5 ;SET "GOOD"                     ;TO BE SET 45: CMP R5,R4 ;COMPARE EXPECTED AND RECEIVED      BEQ 55 ;RESULTS      HLT 1 ;R5=EXPECTED R4=FOUND 55: BIS #STEP,@MC.CSR ;EXAMINE NEXT LINE      DEC R2 ;ALL LINES DONE?      BNE 35 ;BR IF NO      CLR R5 ;CLEAR "GOOD" 65: MOV R1,@MC.CSR ;LOAD LINE      MOV R1,R3 ;SET LINE COUNTER TO SELECTED LINE      CLR @MC.LSR ;CLEAR TERMINAL READY FLIP FLOP      DELAY 104414 ;DELAY FOR CABLE      MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER      TST R4 ;WAS TERMINAL READY FUNCTION FLIP FLOP      BEQ +4 ;CLEARED      HLT 1 ;R5=EXPECTED R4=FOUND      SCOPI      INC R1      DEC R0      BNE 25 75: SCOPE ;CHECK FOR ITERATIONS. LOOP   </pre>
--	--	--	--	---	--

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017110 012737 000C44 001226  
017116 012737 017334 001216  
017124 005737 007256  
017130 001405  
017132 013737 001216 001214  
017140 000177 162050  
017144 005077 170126  
017150 005037 177776  
017154 013700 007274  
017160 005001  
017162 012737 017170 001220  
017170 012777 002900 170100  
017176 012702 000C20  
017202 010177 170070  
017206 010137 007260  
017212 012777 000094 170060  
017220 005077 170052  
017224 005005  
017226 017704 170046  
017232 117703 170040  
017236 042703 177760  
017242 020103  
017244 001002  
017246 012705 000004  
017252 020504  
017254 001401  
017256 104001  
017260 052777 000400 170010  
017266 005302  
017270 001355  
017272 005005  
017274 010177 167776  
017300 010103  
017302 005077 167772  
017306 104414  
017310 017704 167764  
017314 005704  
017316 001401  
017320 104001  
017322 104401  
017324 005201  
017326 005200  
017330 001317  
017332 104400

TEST 44

-----  
TST44: MOV #44,TSTNO  
MOV #TST45,NEXT  
TST TURFLG ;TURN AROUND H861 OR H325?  
SEQ 1\$ ;BR IF H861  
MOV NEXT,RETURN  
JMP @RETURN  
1\$: CLR @MC.CSR ;CLEAR CONTROL STATUS REGISTER  
CLR PS ;ZERO PSW.  
MOV TOTAL,R0 ;SET THE TOTAL NUMBER OF LINES TO BE TESTED IN R  
CLR R1  
MOV #2\$,LOCK  
2\$: MOV #CLRMUX,@MC.CSR ;CLEAR MUX  
MOV #16,,R2 ;SET FOR 16 LINES  
MOV R1,@MC.CSR ;SELECT LINE TO BE TESTED  
MOV R1,LINE ;SET IMAGE  
MOV #R5,@MC.LSR ;SET REQUEST TO SEND FUNCTION FLIP-FLOP  
CLR @MC.CSR ;ZERO CSR  
3\$: CLR R5 ;SET EXPECTED  
MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER  
MOVB @MC.CSR,R3 ;READ CONTROL STATUS REGISTER  
BIC #1C<17>,R3 ;CLEAR UNWANTED BITS  
CMP R1,R3 ;IF LINE NUMBER=SELECTED LINE NUMBER,  
BNE 4\$ ;EXCEPT REQUEST TO SEND FUNCTION FLIP FLOP  
MOV #R5,R5 ;SET "GOOD"  
4\$: CMP R5,R4 ;TO BE SET  
BEQ 5\$ ;COMPARE EXPECTED AND RECEIVED  
HLT 1 ;RESULTS  
5\$: BIS #STEP,@MC.CSR ;R5=EXPECTED R4=FOUND  
DEC R2 ;EXAMINE NEXT LINE  
BNE 3\$ ;ALL LINES DONE?  
CLR R5 ;BR IF NO  
6\$: MOV R1,@MC.CSR ;CLEAR "GOOD"  
MOV R1,R3 ;LOAD LINE  
CLR @MC.LSR ;SET LINE COUNTER TO SELECTED LINE  
DELAY ;CLEAR REQUEST TO SEND FLIP FLOP  
MOV @MC.LSR,R4 ;DELAY FOR CABLE  
TST R4 ;READ LINE STATUS REGISTER  
BEQ .+4 ;WAS REQUEST TO SEND FUNCTION FLIP FLOP  
HLT 1 ;CLEARED  
7\$: SCOPE R1 ;R5=EXPECTED R4=FOUND  
DEC R0  
BNE 2\$  
SCOPE ;CHECK FOR ITERATIONS. LOOP

\*\*\*\*\* TEST 44 \*\*\*\*\*  
\*VERIFY THAT REQUEST TO SEND FUNCTION FLIP-FLOP CAN  
\*BE SET AND CLEARED FOR SELECTED LINE  
\*THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.  
\*MODEM CONTROL LINES \*MUST\* BE CONTIGUOUS FROM LINE 00.  
\*\*\*\*\*



# H06

\*\*\*\*\* TEST 45 \*\*\*\*\*  
: \*VERIFY THAT SECONDARY TRANSMIT FUNCTION FLIP-FLOP CAN  
: \*BE SET AND CLEARED FOR SELECTED LINE  
: \*THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.  
: \*MODEM CONTROL LINES \*MUST\* BE CONTIGUOUS FROM LINE 00.  
: \*\*\*\*\*

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3261 017334 012737 000045 001226  
3262 017342 012737 017560 001216  
3263 017350 005737 007256  
3264 017354 001405  
3265 017356 013737 001216 001214  
3266 017364 000177 161624  
3267 017370 005077 167702 1$: CLR  
3268 017374 005037 177776 CLR  
3269 017400 013700 007274 MOV  
3270 017404 005001 CLR  
3271 017406 012737 017414 001220  
3272 017414 012777 002000 167654 2$: MOV  
3273 017422 012702 000020 MOV  
3274 017426 010177 167644 MOV  
3275 017432 010137 007260 MOV  
3276 017436 012777 000010 167634 MOV  
3277 017444 005077 167626 CLR  
3278 017450 005005 3$: CLR  
3279 017452 017704 167622 MOV  
3280 017456 117703 167614 MOV  
3281 017452 042703 177760 BIC  
3282 017466 020103 CMP  
3283 017470 001002 BNE  
3284 017472 012705 000010 MOV  
3285  
3286 017476 020504 4$: CMP  
3287 017500 001401 BEQ  
3288 017502 104001 HLT  
3289 017504 052777 000400 167554 5$: BIS  
3290 017512 005302 DEC  
3291 017514 001355 BNE  
3292 017516 005005 CLR  
3293 017520 010177 167552 6$: MOV  
3294 017524 010103 MOV  
3295 017526 005077 167546 CLR  
3296 017532 104414 DELAY  
3297 017534 017704 167540 MOV  
3298 017540 005704 TST  
3299 017542 001401 BEQ  
3300 017544 104001 HLT  
3301 017546 104401 SCOP1  
3302 017550 005201 INC  
3303 017552 005300 DEC  
3304 017554 001317 BNE  
3305 017556 104400 7$: SCOPE
```

\*\*\*\*\* TEST 45 \*\*\*\*\*  
: \*VERIFY THAT SECONDARY TRANSMIT FUNCTION FLIP-FLOP CAN  
: \*BE SET AND CLEARED FOR SELECTED LINE  
: \*THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.  
: \*MODEM CONTROL LINES \*MUST\* BE CONTIGUOUS FROM LINE 00.  
: \*\*\*\*\*

TEST 45  
-----  
TST45: MOV #45, TSTNO  
MOV #TST46, NEXT  
TST TURFLG ;TURN AROUND H861 OR H325?  
SEQ 1\$ ;BR IF H861  
MOV NEXT, RETURN  
JMP @RETURN  
1\$: CLR @MC.CSR ;CLEAR CONTROL STATUS REGISTER  
CLR PS ;ZERO PSW  
MOV TOTAL, R0 ;SET THE TOTAL NUMBER OF LINES TO BE TESTED IN R  
CLR R1  
MOV #2\$, LOCK  
2\$: MOV #CLRMUX, @MC.CSR ;CLEAR MUX  
MOV #16, R2 ;SET FOR 16 LINES  
MOV R1, @MC.CSR ;SELECT LINE TO BE TESTED  
MOV R1, LINE ;SET IMAGE  
MOV #SECTX, @MC.LSR ;SET SECONDARY TRANSMIT FUNCTION FLIP-FLOP  
CLR @MC.CSR ;ZERO CSR  
3\$: CLR R5 ;SET EXPECTED  
MOV @MC.LSR, R4 ;READ LINE STATUS REGISTER  
MOVB @MC.CSR, R3 ;READ CONTROL STATUS REGISTER  
BIC #C<17>, R3 ;CLEAR UNWANTED BITS  
CMP R1, R3 ;IF LINE NUMBER=SELECTED LINE NUMBER,  
BNE 4\$ ;EXCEPT SECONDARY TRANSMIT FUNCTION FLIP FLOP  
MOV #SECTX, R5 ;SET "GOOD"  
;TO BE SET  
4\$: CMP R5, R4 ;COMPARE EXPECTED AND RECEIVED  
BEQ 5\$ ;RESULTS  
HLT 1 ;R5=EXPECTED R4=FOUND  
5\$: BIS #STEP, @MC.CSR ;EXAMINE NEXT LINE  
DEC R2 ;ALL LINES DONE?  
BNE 3\$ ;BR IF NO  
CLR "GOOD" ;CLEAR "GOOD"  
6\$: MOV R1, @MC.CSR ;LOAD LINE  
MOV R1, R3 ;SET LINE COUNTER TO SELECTED LINE  
CLR @MC.LSR ;CLEAR SECONDARY TRANSMIT FLIP FLOP  
DELAY ;DELAY FOR CABLE  
MOV @MC.LSR, R4 ;READ LINE STATUS REGISTER  
TST R4 ;WAS SECONDARY TRANSMIT FUNCTION FLIP FLOP  
BEQ +4 ;CLEARED  
HLT 1 ;R5=EXPECTED R4=FOUND  
7\$: SCOPE ;CHECK FOR ITERATIONS. LOOP

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\*\*\*\*\* TEST 46 \*\*\*\*\*  
 \*VERIFY THAT CLEAR TO SEND AND CARRIER ARE SET IF "LINE ENABLE"  
 \*AND TERMINAL ARE SET FOR SELECTED LINE.  
 \*THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.  
 \*MODEM CONTROL LINES \*MUST\* BE CONTIGUOUS FROM LINE 00.  
 \*\*\*\*\*

: TEST 46

```

TST46:  MOV    #46,TSTNO
        MOV    #TST47,NEXT
        TST    TURFLG          ;TURN AROUND H861 OR H325?
        BEQ    1$             ;BR IF H861
        MOV    NEXT,RETURN
        JMP    @RETURN
1$:     CLR    @MC.CSR        ;CLEAR CONTROL REGISTER
        CLR    PS             ;ZERO PSW
        MOV    TOTAL,R0      ;SET THE TOTAL NUMBER OF LINES TO BE TESTED IN R
        CLR    R1
        MOV    #2$,LOCK
        MOV    #16.,R2       ;16 LINES
        MOV    R1,@MC.CSR    ;SELECT A LINE
        MOV    #LINENA+TRMRDY,@MC.LSR ;SET LINE ENABLE +TRMRDY
        CLR    @MC.CSR      ;CLEAR CONTROL REGISTER
        CLR    R5            ;CLEAR EXPECTED RESULT
        MOV    @MC.LSR,R4    ;READ LINE STATUS
        MOVB   @MC.CSR,R3    ;READ LINE NUMBER
        BIC    #1<17>,R3     ;CLEAR UNWANTED BITS
        CMP    R1,R3         ;IF RECEIVED LINE=SELECTED LINE
        BNE    4$           ;EXPECT LINE ENABLE AND
        MOV    #LINENA+TRMRDY+CO+CS,R5 ;CLEAR TO SEND AND CARRIER ARE SET
        CMP    R4,R5        ;COMPARE EXPECTED AND
        BEQ    5$           ;RECEIVED RESULTS
        HLT    1             ;R5=EXPECTED R4=FOUND
        BIS    #STEP,@MC.CSR ;UPDATE LINE COUNTER
        DEC    R2            ;CONTINUE IF ALL CHECKS
        BNE    3$           ;ARE NOT DONE FOR THIS LINE
        MOV    #LINENA,R5    ;EXPECT LINE ENABLE
        MOV    R1,R3         ;ON SELECTED LINE
        MOV    R1,@MC.CSR   ;SELECT LINE
        BIC    #TRMRDY,@MC.LSR ;CLEAR TERMINAL
        DELAY          ;DELAY FOR CABLE
        MOV    @MC.LSR,R4    ;READ LINE STATUS REGISTER
        CMP    R5,R4        ;ONLY LINE ENABLE SHOULD BE
        BEQ    .+4          ;SET ON THIS LINE
        HLT    1             ;R5=EXPECTED R4=FOUND
        SCOPI
        INC    R1
        CLR    @MC.LSR
        DEC    R0
        BNE    2$
7$:     SCOPE                ;CHECK FOR ITERATIONS. LOOP
    
```

# JOB

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```

***** TEST 47 *****
*VERIFY THAT RING IS SET IF "LINE ENABLE"
*AND REQUEST TO SEND ARE SET FOR SELECTED LINE.
*THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.
*MODEM CONTROL LINES *MUST* BE CONTIGUOUS FROM LINE 00.
*****
; TEST 47
-----
TST47: MOV #47,TSTNO
MOV #TST50,NEXT
TST TURFLG ;TURN AROUND H861 OR H325?
BEQ 1$ ;BR IF H861
MOV NEXT,RETURN
JMP @RETURN
1$: CLR @MC.CSR ;CLEAR CONTROL REGISTER
CLR PS ;ZERO PSW
MOV TOTAL,R0 ;SET THE TOTAL NUMBER OF LINES TO BE TESTED IN R
CLR R1
MOV #2$,LOCK
2$: MOV #16,R2 ;16 LINES
MOV R1,@MC.CSR ;SELECT A LINE
MOV #LINENA+RS,@MC.LSR ;SET LINE ENABLE +RS
CLR @MC.CSR ;CLEAR CONTROL REGISTER
CLR R5 ;CLEAR EXPECTED RESULT
MOV @MC.LSR,R4 ;READ LINE STATUS
MOVB @MC.CSR,R3 ;READ LINE NUMBER
BIC #C<17>,R3 ;CLEAR UNWANTED BITS
CMP R1,R3 ;IF RECEIVED LINE=SELECTED LINE
BNE 4$ ;EXPECT LINE ENABLE AND
MOV #LINENA+RS+RING,R5 ;RING IS SET
4$: CMP R4,R5 ;COMPARE EXPECTED AND
BEQ 5$ ;RECEIVED RESULTS
HLT 1 ;R5=EXPECTED R4=FOUND
5$: BIS #STEP,@MC.CSR ;UPDATE LINE COUNTER
DEC R2 ;CONTINUE IF ALL CHECKS
BNE 3$ ;ARE NOT DONE FOR THIS LINE
MOV #LINENA,R5 ;EXPECT LINE ENABLE
6$: MOV R1,R3 ;ON SELECTED LINE
MOV R1,@MC.CSR ;SELECT LINE
BIC #RS,@MC.LSR ;CLEAR REQUEST TO SEND
DELAY ;DELAY FOR CABLE
MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER
CMP R5,R4 ;ONLY LINE ENABLE SHOULD BE
BEQ .+4 ;SET ON THIS LINE
HLT 1 ;R5=EXPECTED R4=FOUND
7$: SCOPE ;CHECK FOR ITERATIONS. LOOP

```

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:***** TEST 50 *****
:*VERIFY THAT SECONDARY RECEIVE IS SET IF "LINE ENABLE"
:*AND SECONDARY TRANSMIT ARE SET FOR SELECTED LINE.
:*THIS TEST IS DONE IF THE H861 TURN AROUND IS USED.
: MODEM CONTROL LINES *MUST* BE CONTIGUOUS FROM LINE 00.
:*****
  
```

; TEST 50

-----

```

1ST50: MOV #50,TSTNO
      MOV #TST51,NEXT
      TST TURFLG ;TURN AROUND H861 OR H325?
      BEQ 1$ ;BR IF H861
      MOV NEXT,RETURN
      JMP @RETURN
1$: CLR @MC.CSR ;CLEAR CONTROL REGISTER
   CLR PSW ;ZERO PSW
   MOV TOTAL,R0 ;SET THE TOTAL NUMBER OF LINES TO BE TESTED IN R
   CLR R1
   MOV #2$,LOCK
2$: MOV #16.,R2 ;16 LINES
   MOV R1,@MC.CSR ;SELECT A LINE
   MOV #LINENA+SECTX,@MC.LSR ;SET LINE ENABLE +SECTX
   CLR @MC.CSR ;CLEAR CONTROL REGISTER
3$: CLR R5 ;CLEAR EXPECTED RESULT
   MOV @MC.LSR,R4 ;READ LINE STATUS
   MOVB @MC.CSR,R3 ;READ LINE NUMBER
   BIC #1C<17>,R3 ;CLEAR UNWANTED BITS
   CMP R1,R3 ;IF RECEIVED LINE=SELECTED LINE
   BNE 4$ ;EXPECT LINE ENABLE AND
   MOV #LINENA+SECTX+SECRX,R5 ;SECONDARY RECEIVE IS SET
4$: CMP R4,R5 ;COMPARE EXPECTED AND
   BEQ 5$ ;RECEIVED RESULTS
   HLT 1 ;R5=EXPECTED R4=FOUND
5$: BIS #STEP,@MC.CSR ;UPDATE LINE COUNTER
   DEC R2 ;CONTINUE IF ALL CHECKS
   BNE 3$ ;ARE NOT DONE FOR THIS LINE
   MOV #LINENA,R5 ;EXPECT LINE ENABLE
6$: MOV R1,R3 ;ON SELECTED LINE
   MOV R1,@MC.CSR ;SELECT LINE
   BIC #SECTX,@MC.LSR ;CLEAR SECONDARY TRANSMIT
   DELAY ;DELAY FOR CABLE
   MOV @MC.LSR,R4 ;READ LINE STATUS REGISTER
   CMP R5,R4 ;ONLY LINE ENABLE SHOULD BE
   BEQ +4 ;SET ON THIS LINE
   HLT 1 ;R5=EXPECTED R4=FOUND
   SCOPI
   INC R1
7$: CLR @MC.LSR
   DEC R0
   BNE 2$
   SCOPE ;CHECK FOR ITERATIONS. LOOP
  
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```
***** TEST 51 *****
*DV11 SINGLE LINE CABLE TEST.
*TEST TO RUN A 5 BIT BLOCK (000-037)
*OF DATA FROM THE DV11 TRANSMITTER INTO THE
*DV11 RECEIVER THROUGH THE CABLE.
*SETUP:
*MODE:          EXTERNAL LOOP BACK
*TXBA:          SYNC
*TXWC:          -42(8)-BIT15
*RXBA:          RXBA
*RXWC:          -40(8)-BIT15
*LINE PROTOCOL TXDDCMP,RXDDCMP,LRC8,STRIP SYNC,IDLE MARK
*LINE STATE    EXPECT BCC,TX GO
*LINE PROGRESS SEND BCC
*NOTE: FOR TEST OF ASYNC LINE CARD:
* "SYNC 'A'" MUST BE SET TO ALL ZEROS
* IN SOFTWARE STATUS MAP.
*
```

```
; TEST 51
-----
TST51:  MOV    #51,TSTNO
        MOV    #TESTER.NEXT
        TST    TURFLG
        BNE    88$
        MOV    NEXT,RETURN
        JMP    @RETURN
88$:   RAMCLR                ;CLEAR DV11
        BIT    #BIT3,LINE    ;DETERMINE LINE NO.
        BEQ    91$
        BIT    #BIT2,LINE
        BEQ    89$
        MOVB   L12.15,SYNC    ;SET SYNC FOR 12-15
        BR    100$
89$:   MOVB   L08.11,SYNC    ;SET SYNC FOR 08-11
        BR    100$
91$:   BIT    #BIT2,LINE
        BEQ    90$
        MOVB   L04.07,SYNC    ;SET SYNC FOR 04-07
        BR    100$
90$:   MOVB   L00.03,SYNC    ;SET SYNC FOR 00-03
100$:  MOVB   SYNC,SYNC+1    ;MAKE SECOND SYNC
        MOV    #TXTAB,R5     ;GET TABLE POINTER
        CLR    R4
101$:  MOVB   #BIT3,(R5)+    ;"INC/BCC" AND "MODE 0"
        INCB   R4           ;ALL DONE?
        BNE    101$        ;BR IF NO
        MOV    #TXTAB,R5     ;SET POINTER
        CLR    R4
        MOVB   SYNC,R4       ;SET SYNC CNTRL BYTE
        BEQ    102$        ;BR IF ASYNC LINE CARD!
        BIC    #C(37),R4
        ADD    R4,R5
102$:  MOVB   #BIT5,(R5)    ;"MODE 1"
        MOV    #TXBAP,R5
```

# M06

3524	020650	005004			CLR R4	
3525	020652	110425			MOV B R4, (R5)+	: LOAD DATA
3526	020654	105204		1\$:	INCB R4	: ALL DONE?
3527	020656	022704	000040		CMP #40, R4	
3528	020662	001373			BNE 1\$	
3529	020664	013777	007260	160500	MOV LINE, @DVSR5	: LOAD LINE NO
3530	020672	105737	023560		TSTB SYNC	: IS THIS AN ASYNC CARD?
3531	020676	001006			BNE 65\$	: BR IF NO
3532	020700	004537	023454		PERFORM SETREG	
3533	020704	000	001		.BYTE 000,001	: TXBAP, BYTE CNT
3534	020706	023562			TXBAP	
3535	020710	077740			<-40>-BIT15	
3536	020712	000405			BR 66\$	
3537	020714	004537	023454	65\$:	PERFORM SETREG	
3538	020720	000	001		.BYTE 000,001	: TX BA, TX BC
3539	020722	023560			SYNC	: SYNC
3540	020724	077736			<-42>-BIT15	: MARKED BYTE COUNT
3541	020726	004537	023454	66\$:	PERFORM SETREG	
3542	020732	004	005		.BYTE 004,005	: RX BA, BC
3543	020734	024562			RXBA	
3544	020736	077740			<-40>-BIT15	
3545	020740	004537	023454		PERFORM SETREG	
3546	020744	012	013		.BYTE 012,013	
3547	020746	000143			BIT6+BIT5+BIT1+BIT0	
3548	020750	002004			BIT10+BIT2	
3549	020752	004537	023454		PERFORM SETREG	
3550	020756	016	014		.BYTE 016,014	
3551	020760	002000			BIT10	
3552	020762	000001			001	: IF SYNC LINE CARD: START IN MODE 1
3553	020764	105737	023560		TSTB SYNC	: IF ASYNC LINE CARD:
3554	020770	001002			BNE +6	: SET TX TO MODE 0
3555	020772	005077	160400		CLR @DVSR4	: WHICH IS TRUE DDCMP MODE!
3556	020776	004537	023454		PERFORM SETREG	
3557	021002	010	010		.BYTE 010,010	
3558	021004	023562			TXTAB-400	
3559	021006	023562			TXTAB-400	
3560	021010	105737	023560		TSTB SYNC	: ASYNC LINE CARD?
3561	021014	001012			BNE 67\$	: BR IF NOT ASYNC
3562	021016	004537	023520		PERFORM LOAD.MODE	
3563	021022	015000			<BIT12+BIT11>+BIT9	: 8 BITS/PER/CHAR.
3564	021024	004537	023520		PERFORM LOAD.MODE	
3565	021030	020000			BIT13	: RX ENABLE
3566	021032	004537	023520		PERFORM LOAD.MODE	
3567	021036	072000			<BIT14+BIT13+BIT12>+BIT10	: ;9600 BAUD.
3568	021040	000403			BR 68\$	
3569	021042	004537	023520	67\$:	PERFORM LOAD.MODE	: MODE FOR CABLE TESTING
3570	021046	030000			BIT13+BIT12	
3571	021050	005277	160306	68\$:	INC @DVSCR	: SET GO
3572	021054	005005			CLR R5	
3573	021056	105777	160300	2\$:	TSTB @DVSCR	: RX BIT7=1?
3574	021062	100404			BMI 3\$	: YES
3575	021064	104414			DELAY	: WASTE TIME
3576	021066	005205			INC R5	: DELAY
3577	021070	001372			BNE 2\$	
3578	021072	104000			HLT	: NO SCR BIT7=1
3579						

# N06

D2DVE MACY11 27(732) 17-SEP-76 14:10 PAGE 79  
 D2DVEB.P11 DV11 DEVICE DIAGNOSTICS. COPYRIGHT 1975 DIGITAL EQUIP. CORP.

3580	021074	013705	007260	3\$:	MOV	LINE,R5	;GET LINE NUMBER
3581	021100	000305			SWAB	R5	;PUT IN HIGH BYTE
3582	021102	052705	050000		BIS	#BIT14+BIT12,R5	
3583	021106	017704	160254		MOV	DVVIC,R4	;READ RIC
3584	021112	020504			CMP	R5,R4	;OK?
3585	021114	001401			BEQ	4\$	;YES
3586	021116	104000			HLT		
3587	021120	005005		4\$:	CLR	R5	
3588	021122	005004			CLR	R4	
3589	021124	012701	023562		MOV	#TXBAP,R1	;CHECK DATA!!
3590	021130	012700	024562		MOV	#RXBA,R0	
3591	021134	012702	000040		MOV	#40,R2	
3592	021140	112004		5\$:	MOV	(R0)+,R4	;GET RX DATA
3593	021142	042704	177740		BIC	#C(37),R4	
3594	021145	112105			MOV	(R1)+,R5	;GET TX DATA
3595	021150	020504			CMP	R5,R4	;OK?
3596	021152	001401			BEQ	6\$	
3597	021154	104000			HLT		;RX DATA BAD!!
3598	021156	005302		6\$:	DEC	R2	;DONE?
3599	021160	001367			BNE	5\$	
3600	021162	104412			MSTCLR		;INIT DV11
3601	021164	104400			SCOPE		;SCOPE TEST.
3602							
3603							
3604							

00010	021166	021166	LOVE=		
00011	021166	021166	=210	JMP	MANUAL
00012	021166	021166	=LOVE		
00013	021166	021166	MANUAL:	MOV	#STACK, SP
00014	021166	021166		MOV	#DV.MAP, R0
00015	021166	021166		CLR	(R0)+
00016	021166	021166	15:	CLR	#DV.END, R0
00017	021166	021166		CMP	IS
00018	021166	021166		BNE	IS
00019	021166	021166		TYPE	MXTITLE
00020	021166	021166		JSR	PC, TKRDY
00021	021166	021166		MOV	SAVRS, DVNUM
00022	021166	021166		BIC	#1C(17), DVNUM
00023	021166	021166		MOV	#1, SAVNUM
00024	021166	021166		MOV	#DV.MAP, R0
00025	021166	021166		MOV	#1, R5
00026	021166	021166	25:	TYPE	MXGIVE
00027	021166	021166		MOV	SAVNUM, SAVR3
00028	021166	021166		CNVRT	,XXLIN
00029	021166	021166		INSTR	,MXSCR
00030	021166	021166		PARAM	
00031	021166	021166			175000
00032	021166	021166			175400
00033	021166	021166			TEMPS
00034	021166	021166		.BYTE	7, 1
00035	021166	021166		MOV	TEMPS, (R0)+
00036	021166	021166		INSTR	,MXVEC
00037	021166	021166		PARAM	
00038	021166	021166			300
00039	021166	021166			770
00040	021166	021166			TEMPS
00041	021166	021166		.BYTE	7, 1
00042	021166	021166		MOV	TEMPS, (R0)+
00043	021166	021166		MOV	SAVNUM, - (SP)
00044	021166	021166	555:	MOV	R5, SAVNUM
00045	021166	021166		TYPE	MXGV
00046	021166	021166		MOV	SAVNUM, SAVR3
00047	021166	021166		CNVRT	,XXLIN
00048	021166	021166		MOV	(SP)+, SAVNUM
00049	021166	021166		TYPE	MXINST
00050	021166	021166		JSR	PC, TKRDY
00051	021166	021166		BIC	#40, SAVRS
00052	021166	021166		CMP	#131, SAVRS
00053	021166	021166		BEQ	, +6
00054	021166	021166		BIS	#BIT15, (R0)
00055	021166	021166		MOV	#226, (R0)
00056	021166	021166		MOV	#62, 2 (R0)
00057	021166	021166		TST	(R0)
00058	021166	021166		BMI	705
00059	021166	021166		TYPE	MASYNC
00060	021166	021166		JSR	PC, TKRDY
00061	021166	021166		BIC	#40, SAVRS
00062	021166	021166		CMP	#116, SAVRS
00063	021166	021166		BEQ	665
00064	021166	021166		MOV	#ASYNC, (R0)
00065	021166	021166		CLR	2 (R0)



3651	021472	000475			BR	705	
3652	021474	104403	022566	655:	INSTR	.MXSY1A	
3653	021500	104405			PARAM		
3654	021502	000001			OO1		
3655	021504	000376			376		
3656	021506	001256			TEMPS		
3657	021510	000	001		.BYTE	0.1	
3658	021512	113710	001256		MOV8	TEMPS,(RO)	
3659	021516	104403	022566		INSTR	.MXSY1B	
3660	021522	104405			PARAM		
3661	021524	000001			OO1		
3662	021526	000376			376		
3663	021530	001256			TEMPS		
3664	021532	000	001		.BYTE	0.1	
3665	021534	113760	001256	000002	MOV8	TEMPS,2(RO)	
3666	021542	104402	022731		TYPE	.MXBITS	
3667	021546	004737	023334		JSR	PC,TKRDY	
3668	021552	042737	177770	001272	BIC	#1(7),SAVRS	
3669	021560	032737	000007	001272	38:	BIT	#7,SAVRS
3670	021566	001422			BEQ	48	
3671	021570	062710	000400		ADD	#400,(RO)	
3672	021574	005237	001272		INC	SAVRS	
3673	021600	000767			BR	38	
3674	021602	104402	023050		TYPE	.MXINST	
3675	021606	004737	023334		JSR	PC,TKRDY	
3676	021612	042737	000040	001272	BIC	#40,SAVRS	
3677	021620	022737	000131	001272	CMP	#131,SAVRS	
3678	021626	001402			BEQ	.+6	
3679	021630	052710	100000		BIS	#BIT15,(RO)	
3680	021634	104402	023174	48:	TYPE	.MXSYN	
3681	021640	004737	023334		JSR	PC,TKRDY	
3682	021644	042737	000040	001272	BIC	#40,SAVRS	
3683	021652	022737	000131	001272	CMP	#131,SAVRS	
3684	021660	001402			BEQ	.+6	
3685	021662	052710	010000		BIS	#BIT12,(RO)	
3686	021666	022020		705:	CMP	(RO)+,(RO)+	
3687	021670	005205			INC	R5	
3688	021672	022705	000005		CMP	#5,R5	
3689	021676	001215			BNE	655	
3700	021700	105237	001303		INCB	SAVNUM	
3701	021704	123737	001303	001301	CMPB	SAVNUM,DVNUM	
3702	021712	101002			BHI	.+6	
3703	021714	000137	021244		JMP	28	
3704	021720	105037	001300		CLRB	DVACTV	
3705	021724	113737	001301	001303	MOV8	DVNUM,SAVNUM	
3706	021732	113701	001301		MOV8	DVNUM,R1	
3707	021736	000241			CLC		
3708	021740	106137	001300		ROLB	DVACTV	
3709	021744	105237	001300		INCB	DVACTV	
3710	021750	105301			DECB	R1	
3711	021752	001371			BNE	.-14	
3712	021754	113737	001300	001302	MOV8	DVACTV,SAVACT	
3713	021762	012710	177777		MOV	#177777,(RO)	
3714	021766	104402	021774		TYPE	.MXFIN	
3715	021772	000000			HALT		
3716	021774						

MXFIN:

D07

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3717 021774 177777 044124 047101 .ASCII <377><377>/THANKS FOR THE INFORMATION./
022031 377 042522 042515 .ASCII <377>/REMEMBER TO START DIAGNOSTIS WITH SW07=!!/
022103 377 042522 040507 .ASCII <377>/REGARDS, JOHN./<212>
022129 377 042523 042514 MSEL: .ASCII <377>/SELECT LINE(S) XXXXXXXXXXXXXXXX/
022165 377 020040 020040 .ASCII <377>/
022206 046377 047111 051505 MLINE: .ASCII <377>/LINES SELECTED(B): <<377>
022235 056 000377 M.CRLF: .ASCII <377>/
022240 051777 047111 046107 MSING: .ASCII <377>/SINGLE LINE: /
022257 .ASCII <377>/
022257 212 053104 030461 .ASCII <212>/DV11 MANUAL PARAMETER INPUT PROGRAM./
022324 050377 042514 051501 .ASCII <377>/PLEASE ANSWER ALL QUESTIONS./
022351 377 054524 042520 .ASCII <377>/TYPE IN NUMBER OF DV11'S IN SYSTEM (1 TO 9): /
022440 043612 053111 020105 MXGIVE: .ASCII <212>/GIVE INFORMATION ON DV11 NO. /
022477 377 054524 042520 MXSCR: .ASCII <377>/TYPE IN THE ADDRESS OF DV11 SYSTEM CONTROL REGISTER:
022566 052377 050131 020105 MXSY1A: .ASCII <377>/TYPE IN SYNC "A" FOR LINE CARD: /
022630 052377 050131 020105 MXVEC: .ASCII <377>/TYPE IN VECTOR "A" FOR DV11: /
022667 377 054524 042520 MXSY1B: .ASCII <377>/TYPE IN SYNC "B" FOR LINE CARD: /
022731 377 054524 042520 MXBITS: .ASCII <377>/TYPE IN BITS-PER-CHAR FOR LINE CARD: /
023000 043612 053111 020105 MXGV: .ASCII <212>/GIVE INFORMATION FOR LINE CARD NUMBER /
023050 044777 020123 044124 MXINST: .ASCII <377>/IS THIS LINE CARD INSTALLED?(Y OR N) /
023117 377 051511 052040 MASYNC: .ASCII <377>/IS THIS AN ASYNCHRONOUS LINE CARD?(Y OR N) /
023174 040777 042522 054440 MXSYN: .ASCII <377>/ARE YOU JUMPERED FOR TWO SYNCs? (Y OR N) /
023247 377 040450 020051 MTURN: .ASCII <377>/ (A) H325 <<377>/ (B) H861. <377>/TYPE "A" OR "B": /
023314 046777 042117 046505 MVECZ: .ASCII <377>/MODEM VECTOR: /
023334 105777 155644 .EVEN
023340 100375 TKRDY: TSTB JTKCSR
023342 017746 155640 BPL -4
023346 042716 000200 MOV JTKCSR, -(SP)
023352 032716 000100 BIC #BIT7, (SP)
023356 001402 BEQ #BIT6, (SP) :CHAR OR NUMBER
023360 042716 000040 BIC #BITS, (SP) :BR IF NUMBER
023354 022716 000015 CMP #15, (SP) :MAKE UPPER CASE
023370 001411 BEQ 15
023372 011637 001272 MOV (SP), SAVRS
023376 105777 155606 TSTB JTKCSR
023402 100375 BPL -4
023404 011677 155602 MOV (SP), JTKCSR
023410 005726 TST (SP)+
023412 000750 BR TKRDY
023414 005726 15: TST (SP)+
023416 000207 RTS PC
023420 000001 YX-IN: 1
023422 002 001 .BYTE 2,1
023424 001266 SAVRS

CKBIT15:
MOV RO, -(SP)
CLR RO
64$: TST J0VLCR
BPL 65$
DELAY
INC RO
BNE 64$
HLT 0
65$: MOV (SP)+, PC
:BIT 15 FAILED TO CLEAR

```

E07

02331 023452 000207  
02332 023454 010046  
02333 023456 010146  
02334 023460 112500  
02335 023462 112501  
02336 023464 110077 155704  
02337 023470 012577 155702  
02338 023474 042777 000060 155660  
02339 023502 110177 155666  
02340 023506 012577 155664  
02341 023512 012601  
02342 023514 012600  
02343 023516 000205  
  
023520  
023520 012577 155644  
023524 052777 100000 155636  
023532 010046  
023534 005000  
023536 005777 155526  
023542 100004  
023544 104414  
023546 005200  
023550 001372  
023552 104000  
023554 012600  
023556 000205  
023560 000001  
023562 000400  
024162 000400  
024562 000400  
025162 051777 047111 046107  
025227 377 040503 046102  
025306 046777 042117 046505  
025333 377 054105 042520  
025366 052777 042516 050130  
025432 046777 042117 046505  
025474 051377 040505 044504  
025550 042777 050130 041505

000207  
010046  
010146  
112500  
112501  
110077 155704  
012577 155702  
042777 000060 155660  
110177 155666  
012577 155664  
012601  
012600  
000205  
  
012577 155644  
052777 100000 155636  
010046  
005000  
005777 155526  
100004  
104414  
005200  
001372  
104000  
012600  
000205  
000001  
000400  
000400  
000400  
047111 046107  
377 040503 046102  
046777 042117 046505  
377 054105 042520  
052777 042516 050130  
046777 042117 046505  
051377 040505 044504  
042777 050130 041505

SETREG: R/S PC  
MOV RO, -(SP)  
MOV RI, -(SP)  
MOVB (R5)+, RO  
MOVB (R5)+, RI  
MOVB RO, 2DVSRSH  
MOV (R5)+, 2DVSRA  
BIC #BIT5+BIT4, 2DVSCR  
MOVB RI, 2DVSRSH  
MOV (R5)+, 2DVSRA  
MOV (SP)+, RI  
MOV (SP)+, RO  
EXIT  
  
LOAD.MODE:  
MOV (R5)+, 2DVLOR  
BIS #BIT15, 2DVLOR  
MOV RO, -(SP)  
CLR RO  
15: TST 2DVLOR  
BPL 25  
DELAY  
INC RO  
BNE 15  
HLT 0 ;BIT 15 FAILED TO CLEAR  
25: MOV (SP)+, RO  
EXIT  
  
SYNC: .BLKW 1  
TXBAP: .BLKB 400  
TXTAB: .BLKB 400  
RXBA: .BLKB 400  
EM1: .ASCIZ <377>/SINGLE LINE CABLE TESTS(DV11 ERROR)/  
EM2: .ASCIZ <377>/CABLE TURN AROUND TESTS (MODEM CONTROL ERROR)/  
EM3: .ASCIZ <377>/MODEM CONTROL ERROR/  
EM4: .ASCIZ <377>/EXPECTED FOUND REGISTER/  
EM5: .ASCIZ <377>/UNEXPECTED MODEM CONTROL INTERRUPT./  
EM6: .ASCIZ <377>/MODEM CONTROL FAILED TO INTERRUPT/  
EM7: .ASCIZ <377>/READING MODEM CONTROL CAUSED AT TRAP TO 4./  
EM8: .ASCIZ <377>/EXPECTED FOUND LINE DVSCR MC.CSR  
EVEN  
DT1: 5  
.BYTE 6.4  
SAVR5  
.BYTE 6.1  
SAVR4  
.BYTE 2.4  
LINE  
.BYTE 6.1  
DVSCR  
.BYTE 6.1  
MC.CSR  
DT2: 3  
.BYTE 6.4  
SAVR5  
.BYTE 6.1  
SAVR4

;BIT 15 FAILED TO CLEAR

DT1: 5  
.BYTE 6.4  
SAVR5  
.BYTE 6.1  
SAVR4  
.BYTE 2.4  
LINE  
.BYTE 6.1  
DVSCR  
.BYTE 6.1  
MC.CSR  
DT2: 3  
.BYTE 6.4  
SAVR5  
.BYTE 6.1  
SAVR4

F07

3	025556	006	001	.BYTE	E.1
0	025560	001266		SAVR3	
0	025562			.ERRTAB:	
0	025562	025162		EM1	
0	025564	025550		DH1	
0	025566	025616		DT1	
0	025570	025227		FM2	
0	025572	025550		DH1	
0	025574	025616		DT1	
0	025576	025308		FM3	
0	025700	025333		DH4	
0	025702	025544		DT2	
0	025704	025366		FM4	
0	025706	000000			
0	025710	000000			
0	025712	025432			
0	025714	000000			
0	025716	000000			
0	025720	025474			
0	025722	000000			
0	025724	000000			
0	025726	000000			
0	025730	000000			
0	025732	000000			
0	025734	000000			

.ERRTAB:

.END





DV04.C	001534	959#												
DV04.D	001640	861#												
DV05.A	001650	866#												
DV05.B	001654	868#												
DV05.C	001660	870#												
DV05.D	001664	872#												
DV06.A	001674	877#												
DV06.B	001700	879#												
DV06.C	001704	881#												
DV06.D	001710	882#												
DV07.A	001720	888#												
DV07.B	001724	890#												
DV07.C	001730	892#												
DV07.D	001734	894#												
EM1	025162	3762#	3781											
EM2	025222	3762#	3784											
EM3	025306	3762#	3787											
EM4	025366	3762#	3790											
EM5	025432	3762#	3793											
EM6	025474	3762#	3796											
ERRCNT	001232	670#	915*	1037	1355*									
ERRFLG	001311	705#	911*	999*	1066*	1307*	1320	1334*	1389*					
ERRMSG	004252	1317*	1335	1338#										
ERTABO	004366	1332	1364#											
EXERCI	007272	1773#	1944*	1947*	1965*									
EXIT =	000205	609#	3743	3757										
EXITER	004322	1350	1355#											
FIX.OO	006516	1574	1579	1584	1589	1623#								
HALTS	004302	1303	1349#											
HILIM	003436	1142*	1169	1187#										
ICOUNT	001222	666#	1064	1069*										
INBUF	005520	1112	1148	1495#										
INIFLG	001310	704#	920	935*										
INSTR=	104404	727#	1163											
INSTR =	104403	725#	1596	1876	1930	3624	3631	3662	3669					
INSTR2	003236	1119	1131#											
INTENA=	000100	1743#	2039	2041	2042	2046	2047	2049	2198	2218	2242	2263	2284	2305
		2326	2347	2369	2389	2654	2732							
KBIAR	010274	1855	1977	1982#										
LIGHT	000174	638#	930											
LIGHTS	001200	643#	930*	1001*										
LIMITS	003364	1159	1169#											
LINE	007260	1768#	1880	1883*	1945*	1946*	1966*	2779	2825	2872	2919	2967	3014	3061
		3113*	3167*	3221*	3275*	3497	3499	3505	3530	3580	3769			
LINENA=	000001	1756#	2617	2647	2782	2790	2970	2979	2996	3017	3025	3033	3064	3072
		3080	3114	3122	3329	3337	3345	3383	3391	3399	3437	3445	3453	
LOAD.M	023520	3563	3565	3567	3570	3745#								
LOBITS	003442	1144*	1173	1189#	1190									
LOCK	001220	655#	1068*	1082	1084	1326	2405*	2431*	2461*	2483*	2510*	2554*	2597*	2639*
		2694*	3109*	3163*	3217*	3271*	3326*	3380*	3434*					
LOGICA	002560	635	1018#											
LOKFLG	001312	706#												
LCLIM	003434	1141*	1171	1186#										
LOVE =	021166	3605#	3609											
LPCNT	001224	667#	1063*	1064	1067*									
-STERR	001234	671#	916*	998*	1050*	1304	1306*	1390*						







RING = 000200  
RINGF = 100000  
ROMCLK = 104415  
RS = 000004  
RUN 001304  
RXBA 024562  
RC = %000000

	1765*	2978	3391											
	1752*													
	745*													
	1760*	2876	2884	3017	3025	3036	3222	3230	3383	3391	3402			
	697*	914*	1515	1518*	1519*	1526*	1527*							
	3544	3590	3761*											
	569*	953*	961*	962*	964*	966*	968	969	1051	1057*	1071*	1204	1209*	
	1221	1234*	1238*	1248	1264*	1352*	1397	1398*	1399*	1401*	1427	1428*	1433*	
	1436*	1528*	1534	1535	1536	1537	1538	1539	1540	1541	1542	1543	1544*	
	1550	1552	1558	1560	1562	1565	1567	1569	1571*	1576*	1581*	1586*	1604*	
	1605	1608	1610	1612	1615	1616	1623	1642	1703*	1712*	1714*	1716	1724*	
	1725*	1893*	1894	1895*	1896	1900	1902	1904	1906*	1922	1982	1983*	1984*	
	1985	1988*	2013*	2410*	2418*	2436*	2446*	2465*	2468*	2470*	2481*	2487*	2499*	
	2514*	2541*	2558*	2585*	2601*	2604*	2607*	2625*	2644*	2648*	2653*	2690*	2695*	
	2698*	2701*	2704*	2707*	2708	2711	2717*	2723*	2728*	2758*	3107*	3141*	3161*	
	3195*	3215*	3249*	3269*	3303*	3324*	3357*	3378*	3411*	3432*	3465*	3590*	3592	
	3610*	3611*	3612	3619*	3630*	3637*	3649*	3650*	3651*	3652	3659*	3660*	3668*	
	3675*	3681*	3689*	3695*	3696	3713*	3722	3723*	3727*	3730*	3732	3734*	3736	
	3742*	3748	3749*	3753*	3756*									
R1 = %000001	570*	965*	966	967*	968	1015*	1019	1203	1210*	1222	1226*	1228	1229	
	1230	1231	1263*	1407	1409*	1412*	1415*	1572*	1577*	1582*	1587*	1627*	1632*	
	1637*	1640*	1663*	1665	1667	1669	1672	1685*	1686	1692*	1693	1697*	1713*	
	1714	1715*	1716	1717	2526*	2530	2538*	2570*	2574	2582*	2778*	2781	2788	
	2799	2800	2825*	2828	2835	2846	2847	2872*	2875	2882	2892	2894	2919*	
	2922	2929	2940	2941	2967*	2969	2976	2987	2988	3014*	3016	3023	3034	
	3035	3061*	3063	3070	3081	3082	3108*	3112	3113	3120	3131	3132	3140*	
	3162*	3166	3167	3174	3185	3186	3194*	3216*	3220	3221	3228	3239	3240	
	3248*	3270*	3274	3275	3282	3293	3294	3302*	3325*	3328	3335	3346	3347	
	3355*	3379*	3382	3389	3400	3401	3409*	3433*	3436	3443	3454	3455	3463*	
	3589*	3594	3706*	3710*	3733	3735*	3739	3741*						
R2 = %000002	571*	1202	1211*	1573*	1578*	1583*	1588*	1628*	1633*	1638*	1641*	1644*	1657*	
	1658*	1659	1662*	1672*	1673	1674*	1675*	1676*	1677*	1678*	1679*	1680*	1681*	
	1688*	1711*	1723*	1727*	1728*	1729*	1730*	1732*	1733*	2008*	2011*	2515*	2520	
	2525*	2531	2559*	2565	2569*	2575	2780*	2796*	2827*	2843*	2874*	2890*	2921*	
	2937*	2968*	2984*	3015*	3031*	3062*	3078*	3111*	3128*	3165*	3182*	3219*	3236*	
	3273*	3290*	3327*	3343*	3381*	3397*	3435*	3451*	3591*	3598*				
R3 = %000003	572*	1106	1113*	1123*	1126*	1128	1132*	1201	1212*	1223	1235*	1236*	1237*	
	1238	1247*	1248*	1253*	1256*	1262*	1623*	1624*	1625	1630	1635	2014*	2015	
	2019*	2019	2038*	2039*	2040	2047*	2048	2065*	2066*	2067	2074*	2075	2092*	
	2093*	2094	2101*	2102	2119*	2120*	2121	2129*	2129	2145*	2146*	2147	2153*	
	2156	2406*	2407*	2411*	2412	2432*	2434*	2437*	2438*	2440	2462*	2463*	2466*	
	2467*	2472*	2473*	2475	2484*	2485	2489*	2490*	2492	2511*	2512*	2516*	2517	
	2519*	2520*	2521*	2522*	2524*	2527*	2529	2555*	2556*	2561*	2562*	2565*	2566*	
	2568*	2571*	2573	2598*	2599*	2603*	2608*	2609*	2640*	2641*	2642*	2645*	2646*	
	2654*	2656*	2659	2668	2673	2678*	2716*	2718*	2719*	2722*	2725*	2726	2732*	
	2734*	2737	2746	2751	2756*	2786*	2787*	2788	2800*	2833*	2834*	2835	2847*	
	2880*	2881*	2882	2894*	2927*	2928*	2929	2941*	2974*	2975*	2976	2987*	3021*	
	3022*	3023	3034*	3068*	3069*	3070	3081*	3118*	3119*	3120	3132*	3172*	3173*	
	3174	3186*	3226*	3227*	3228	3240*	3280*	3281*	3292	3294*	3333*	3334*	3335	
	3346*	3387*	3388*	3389	3400*	3441*	3442*	3443	3454*					
R4 = %000004	573*	1107	1112*	1116*	1117*	1118	1125*	1129	1131*	1139	1148*	1149	1151	
	1153	1155*	1156	1157	1178*	1179*	1183*	1200	1213*	1224	1232*	1235	1240*	
	1242*	1244*	1261*	1311*	1312*	1313*	1314*	1315*	1316*	1317	1318	1319	1408	
	1410*	1411*	1414*	2015*	2019*	2040*	2041*	2043	2048*	2049*	2050	2067*	2068*	
	2070	2075*	2076*	2077	2094*	2095*	2097	2102*	2103*	2104	2121*	2122*	2124	
	2129*	2130*	2131	2147*	2148	2150	2156*	2157	2159	2412*	2413	2440*	2441	

M07

		2475*	2476	2492*	2493	2529*	2531	2534	2573*	2575	2578	2610*	2612	2618*
		2619	2668*	2673*	2674	2746*	2751*	2752	2785*	2792	2803*	2804	2832*	2839
		2850*	2851	2879*	2886	2997*	2898	2926*	2933	2944*	2945	2973*	2980	2991*
		2992	3020*	3027	3038*	3039	3067*	3074	3085*	3086	3117*	3124	3135*	3136
		3171*	3178	3189*	3190	3225*	3232	3243*	3244	3279*	3286	3297*	3298	3332*
		3339	3350*	3351	3386*	3393	3404*	3405	3440*	3447	3458*	3459	3512*	3514*
		3517*	3518*	3520*	3521	3525*	3526	3527*	3528	3583*	3584	3588*	3592*	3593*
		3595												
RS	=%000005	574*	1090	1091*	1095	1100	1102*	1138	1140*	1141	1142	1143	1144	1145
		1145	1147*	1156*	1159*	1160*	1161*	1169	1171	1173	1179	1180*	1184*	1199
		1214*	1225	1233*	1245*	1260*	1309*	1310*	1311	1313	1912*	1915*	2005*	2042*
		2043	2046*	2050	2069*	2070	2073*	2077	2096*	2097	2100*	2104	2123*	2124
		2127*	2131	2148*	2149*	2150	2157*	2158*	2159	2409*	2411	2413	2417*	2435*
		2441	2445*	2471*	2476	2480*	2488*	2493	2497*	2530*	2533*	2534	2574*	2577*
		2578	2611*	2616*	2619	2650*	2674	2679*	2729*	2752	2757*	2784*	2790*	2792
		2798*	2831*	2837*	2839	2845*	2878*	2884*	2886	2892*	2925*	2931*	2933	2939*
		2972*	2978*	2980	2986*	2992	3019*	3025*	3027	3033*	3039	3066*	3072*	3074
		3080*	3086	3116*	3122*	3124	3130*	3170*	3176*	3178	3184*	3224*	3230*	3232
		3238*	3278*	3284*	3286	3292*	3331*	3337*	3339	3345*	3351	3385*	3391*	3393
		3399*	3405	3439*	3445*	3447	3453*	3459	3511*	3513*	3516*	3521*	3522*	3523*
		3526*	3573*	3577*	3580*	3581*	3582*	3584	3587*	3594*	3595	3620*	3639	3697*
		3698	3734	3735	3737	3740	3746							
SAVACT	001302	695*	955	1700*	3712*									
SAVNUM	001303	696*	909*	1011*	1014*	1693*	3618*	3622	3638	3639*	3641	3643*	3700*	3701
		3705*												
SAVPC	001276	692*	1195*	1366										
SAVRO	001260	685*	1204*	1209										
SAVR1	001262	686*	1203*	1210										
SAVR2	001264	687*	1202*	1211										
SAVR3	001266	688*	1201*	1212	1911*	1918*	1919	3622*	3641*	3719	3779			
SAVR4	001270	689*	1200*	1213	2658*	2664*	2736*	2742*	3766	3777				
SAVR5	001272	690*	1199*	1214	1864	1868	3616	3646*	3647	3656*	3657	3678*	3679	3682*
		3686*	3687	3692*	3693	3717*	3764	3775						
SAVSP	001274	691*												
SAVSE =	104406	731*	1308											
SCNENA =	000040	1742*	2120	2122	2123	2127	2128	2130	2146	2153	2656	2678	2734	2756
SCOPE =	104400	719*	2023	2053	2080	2107	2134	2162	2183	2204	2226	2248	2269	2290
		2311	2332	2353	2374	2395	2420	2448	2500	2543	2587	2627	2682	2760
		2807	2854	2901	2948	2995	3042	3089	3143	3197	3251	3305	3359	3413
		3467	3601											
SCOP1 =	104401	721*	2416	2444	2479	2496	2537	2581	2615	2622	2670	2677	2748	2755
		3139	3193	3247	3301	3354	3408	3462						
		1758*	3445											
SECRX =	000020	1749*												
SECRXF =	010000	1757*	3276	3284	3437	3445	3456							
SECTX =	000010	1772*	1871*	1882*	1886*	1889	1890*	1898*	1907*	1909	1912	1944	1965	
SELECT	007270	1049	1296	1440*	1441									
SERV.G	004640	3533	3538	3542	3546	3550	3557	3732*						
SETREG	023454	575*	907*	922*	923*	929	932	933	976*	1045*	1046*	1047	1051*	1071
SP =	%000006	1084*	1090*	1091	1092*	1102	1106*	1107*	1108	1109*	1125	1126	1128*	1129*
		1131	1132	1138*	1139*	1140	1146*	1183	1184	1195	1221*	1222*	1223*	1224*
		1225*	1226	1227*	1260	1261	1262	1263	1264	1277*	1278*	1279*	1280*	1281*
		1282*	1283*	1284	1292*	1293*	1294	1304	1306	1309	1352	1361*	1383*	1397*
		1401	1407*	1408*	1414	1415	1427*	1436	1449*	1450*	1451	1456	1462	1464
		1466*	1467	1470*	1471*	1472	1706*	1732	1734	1735*	1970*	1971*	1974*	1975*
		1976	1978	1982*	1987*	1989	2025*	3609*	3638*	3643	3717*	3722*	3730	3732*









2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100	2101	2102	2103	2104	2105	2106	2107	2108	2109	2110	2111	2112	2113	2114	2115	2116	2117	2118	2119	2120	2121	2122	2123	2124	2125	2126	2127	2128	2129	2130	2131	2132	2133	2134	2135	2136	2137	2138	2139	2140	2141	2142	2143	2144	2145	2146	2147	2148	2149	2150	2151	2152	2153	2154	2155	2156	2157	2158	2159	2160	2161	2162	2163	2164	2165	2166	2167	2168	2169	2170	2171	2172	2173	2174	2175	2176	2177	2178	2179	2180	2181	2182	2183	2184	2185	2186	2187	2188	2189	2190	2191	2192	2193	2194	2195	2196	2197	2198	2199	2200	2201	2202	2203	2204	2205	2206	2207	2208	2209	2210	2211	2212	2213	2214	2215	2216	2217	2218	2219	2220	2221	2222	2223	2224	2225	2226	2227	2228	2229	2230	2231	2232	2233	2234	2235	2236	2237	2238	2239	2240	2241	2242	2243	2244	2245	2246	2247	2248	2249	2250	2251	2252	2253	2254	2255	2256	2257	2258	2259	2260	2261	2262	2263	2264	2265	2266	2267	2268	2269	2270	2271	2272	2273	2274	2275	2276	2277	2278	2279	2280	2281	2282	2283	2284	2285	2286	2287	2288	2289	2290	2291	2292	2293	2294	2295	2296	2297	2298	2299	2300	2301	2302	2303	2304	2305	2306	2307	2308	2309	2310	2311	2312	2313	2314	2315	2316	2317	2318	2319	2320	2321	2322	2323	2324	2325	2326	2327	2328	2329	2330	2331	2332	2333	2334	2335	2336	2337	2338	2339	2340	2341	2342	2343	2344	2345	2346	2347	2348	2349	2350	2351	2352	2353	2354	2355	2356	2357	2358	2359	2360	2361	2362	2363	2364	2365	2366	2367	2368	2369	2370	2371	2372	2373	2374	2375	2376	2377	2378	2379	2380	2381	2382	2383	2384	2385	2386	2387	2388	2389	2390	2391	2392	2393	2394	2395	2396	2397	2398	2399	2400	2401	2402	2403	2404	2405	2406	2407	2408	2409	2410	2411	2412	2413	2414	2415	2416	2417	2418	2419	2420	2421	2422	2423	2424	2425	2426	2427	2428	2429	2430	2431	2432	2433	2434	2435	2436	2437	2438	2439	2440	2441	2442	2443	2444	2445	2446	2447	2448	2449	2450	2451	2452	2453	2454	2455	2456	2457	2458	2459	2460	2461	2462	2463	2464	2465	2466	2467	2468	2469	2470	2471	2472	2473	2474	2475	2476	2477	2478	2479	2480	2481	2482	2483	2484	2485	2486	2487	2488	2489	2490	2491	2492	2493	2494	2495	2496	2497	2498	2499	2500	2501	2502	2503	2504	2505	2506	2507	2508	2509	2510	2511	2512	2513	2514	2515	2516	2517	2518	2519	2520	2521	2522	2523	2524	2525	2526	2527	2528	2529	2530	2531	2532	2533	2534	2535	2536	2537	2538	2539	2540	2541	2542	2543	2544	2545	2546	2547	2548	2549	2550	2551	2552	2553	2554	2555	2556	2557	2558	2559	2560	2561	2562	2563	2564	2565	2566	2567	2568	2569	2570	2571	2572	2573	2574	2575	2576	2577	2578	2579	2580	2581	2582	2583	2584	2585	2586	2587	2588	2589	2590	2591	2592	2593	2594	2595	2596	2597	2598	2599	2600	2601	2602	2603	2604	2605	2606	2607	2608	2609	2610	2611	2612	2613	2614	2615	2616	2617	2618	2619	2620	2621	2622	2623	2624	2625	2626	2627	2628	2629	2630	2631	2632	2633	2634	2635	2636	2637	2638	2639	2640	2641	2642	2643	2644	2645	2646	2647	2648	2649	2650	2651	2652	2653	2654	2655	2656	2657	2658	2659	2660	2661	2662	2663	2664	2665	2666	2667	2668	2669	2670	2671	2672	2673	2674	2675	2676	2677	2678	2679	2680	2681	2682	2683	2684	2685	2686	2687	2688	2689	2690	2691	2692	2693	2694	2695	2696	2697	2698	2699	2700	2701	2702	2703	2704	2705	2706	2707	2708	2709	2710	2711	2712	2713	2714	2715	2716	2717	2718	2719	2720	2721	2722	2723	2724	2725	2726	2727	2728	2729	2730	2731	2732	2733	2734	2735	2736	2737	2738	2739	2740	2741	2742	2743	2744	2745	2746	2747	2748	2749	2750	2751	2752	2753	2754	2755	2756	2757	2758	2759	2760	2761	2762	2763	2764	2765	2766	2767	2768	2769	2770	2771	2772	2773	2774	2775	2776	2777	2778	2779	2780	2781	2782	2783	2784	2785	2786	2787	2788	2789	2790	2791	2792	2793	2794	2795	2796	2797	2798	2799	2800	2801	2802	2803	2804	2805	2806	2807	2808	2809	2810	2811	2812	2813	2814	2815	2816	2817	2818	2819	2820	2821	2822	2823	2824	2825	2826	2827	2828	2829	2830	2831	2832	2833	2834	2835	2836	2837	2838	2839	2840	2841	2842	2843	2844	2845	2846	2847	2848	2849	2850	2851	2852	2853	2854	2855	2856	2857	2858	2859	2860	2861	2862	2863	2864	2865	2866	2867	2868	2869	2870	2871	2872	2873	2874	2875	2876	2877	2878	2879	2880	2881	2882	2883	2884	2885	2886	2887	2888	2889	2890	2891	2892	2893	2894	2895	2896	2897	2898	2899	2900	2901	2902	2903	2904	2905	2906	2907	2908	2909	2910	2911	2912	2913	2914	2915	2916	2917	2918	2919	2920	2921	2922	2923	2924	2925	2926	2927	2928	2929	2930	2931	2932	2933	2934	2935	2936	2937	2938	2939	2940	2941	2942	2943	2944	2945	2946	2947	2948	2949	2950	2951	2952	2953	2954	2955	2956	2957	2958	2959	2960	2961	2962	2963	2964	2965	2966	2967	2968	2969	2970	2971	2972	2973	2974	2975	2976	2977	2978	2979	2980	2981	2982	2983	2984	2985	2986	2987	2988	2989	2990	2991	2992	2993	2994	2995	2996	2997	2998	2999	3000
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# JOB

DZDVE MACY11 27:732' 17-SEP-76 14:10 PAGE 104  
 DZDVEB.P11 CROSS REFERENCE TABLE -- PERMANENT SYMBOLS

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	909	1847	1850	1992	1998	2001	2029	2034	2037	2056	2061	2064	2083	2088	2091
	2110	2115	2118	2136	2141	2144	2164	2169	2172	2185	2190	2193	2206	2211	2214
	2229	2234	2237	2250	2255	2258	2271	2276	2279	2292	2297	2300	2313	2318	2321
	2334	2339	2342	2355	2360	2363	2376	2381	2384	2396	2401	2404	2422	2427	2430
	2449	2457	2460	2501	2506	2509	2544	2550	2553	2588	2593	2596	2629	2635	2638
	2683	2690	2693	2761	2768	2771	2808	2815	2818	2855	2862	2865	2902	2903	2912
	2950	2957	2960	2997	3004	3007	3044	3051	3054	3090	3097	3100	3144	3151	3154
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	3468	3488	3787												
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	731	733	735	737	739	741	743	745	747	749	804	898	991	1039	1486
	1739	1779	1793	1851	1991	1992	1996	2002	2029	2032	2038	2056	2059	2065	2093
	2086	2092	2110	2113	2119	2136	2139	2145	2164	2167	2173	2185	2188	2194	2206
	2209	2215	2229	2232	2238	2250	2253	2259	2271	2274	2290	2292	2295	2301	2313
	2316	2322	2334	2337	2343	2355	2358	2364	2376	2379	2385	2396	2399	2406	2422
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	3420	3426	3468	3486	3492	3717	3762	3787							
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.LIST	731	733	735	737	739	741	743	745	747	749	804	898	991	1039	1486
	1739	1779	1793	1851	1991	1992	1996	2002	2029	2032	2038	2056	2059	2065	2093
	2086	2092	2110	2113	2119	2136	2139	2145	2164	2167	2173	2185	2188	2194	2206
	2209	2215	2229	2232	2238	2250	2253	2259	2271	2274	2290	2292	2295	2301	2313
	2316	2322	2334	2337	2343	2355	2358	2364	2376	2379	2385	2396	2399	2406	2422
	2425	2432	2449	2455	2462	2501	2504	2511	2544	2548	2555	2588	2591	2598	2629
	2633	2640	2683	2688	2695	2761	2766	2772	2808	2813	2819	2855	2860	2866	2902
	2907	2913	2950	2955	2961	2997	3002	3008	3044	3049	3055	3090	3095	3101	3144
	3149	3155	3198	3203	3209	3252	3257	3263	3307	3312	3318	3361	3366	3372	3415
	3420	3426	3468	3486	3492	3717	3762	3787							
.PAGE	564	611	700	752	804	898	991	1501	1739	1793	1847	2228	2270	2312	2354
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.WORD	1483	1927													

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0

\* DZDVEB.SEG/SOL/CRF/DS:ERFZ=DZDVEB.MAC,DZDVEB.P11  
 RUN-TIME: 27 42 7 SECONDS  
 RUN-TIME RATIO: 338/78=4.3  
 CORE USED: 19K (37 PAGES)

